RK3566_G(OSC/PLL/PMUIO1/2)

**PMU1 Domain**
Operating Voltage=1.8V Only

**PMU2 Domain**
Operating Voltage=1.8V/3.3V

**PMUIO1/2/OSC Domain Logic Power**
Operating Voltage=0.9V

**Note:**
Caps of between dashed green lines and U1000 should be placed under the U1000 package.
Other caps should be placed close to the U1000 package.
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Caps of between dashed green lines and U1000 should be placed under the U1000 package.
Other caps should be placed close to the U1000 package.
### RK3566_P (MIPI_CSI_RX)

![MIPI_CSI_RX Diagram](image_url)

### RK3566_M (VCCIO6 Domain)

![VCCIO6 Domain Diagram](image_url)

### Mode Table

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</table>

### Note:
- Caps of between dashed green lines and U1000 should be placed under the U1000 package.
- Other caps should be placed close to the U1000 package.

### Operating Voltage
- VCCIO6 Domain: 1.8V/3.3V
Note:
Caps of between dashed green lines and U1000 should be placed under the U1000 package. Other caps should be placed close to the U1000 package.
**VCCIO5 Domain**

Operating Voltage=1.8V/3.3V

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**Note:**
Caps of between dashed green lines and U1000 should be placed under the U1000 package.
RK3566\_H(VCCIO1 Domain)

VCCIO1 Domain
Operating Voltage=1.8V/3.3V
VDD_CPU_EXT

Feedback from RKSoC

I2C0_SCL_PMIC R99 1 2 22R 5% SDA_EXTDCDC
I2C0_SDA_PMIC R100 1 2 22R 5% SCL_EXTDCDC
PMIC_SLEEP_H R101 1 2 22R 5% VSEL_EXTDCDC
R102 1 2 NC 5% R0201
Note:
FLASH_VOL_SEL state decided to VCCIO2 domain IO driven by default
Logic=L: 3.3V IO driven
Logic=H: 1.8V IO driven
Headphone (Reserved)
Note: 3.3V1.5 A