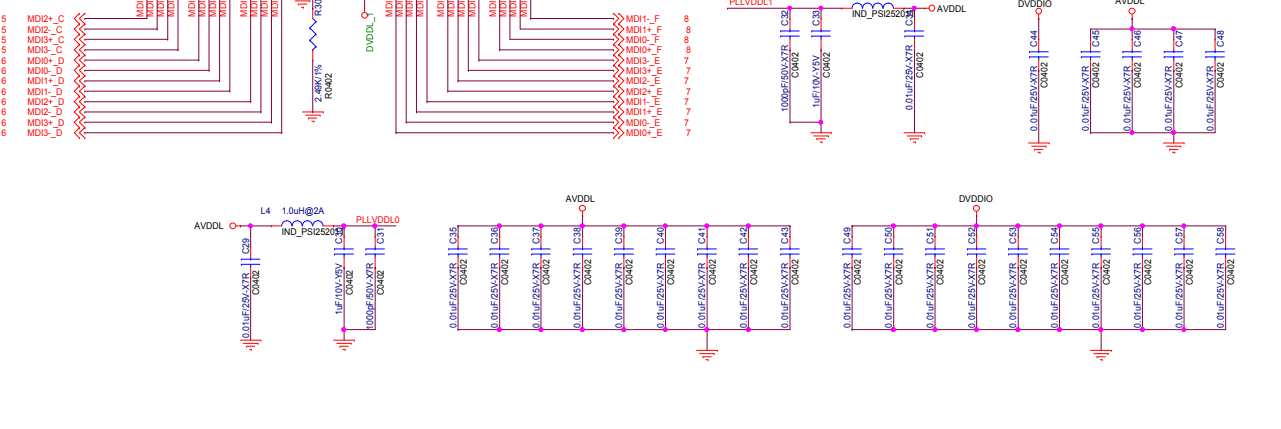
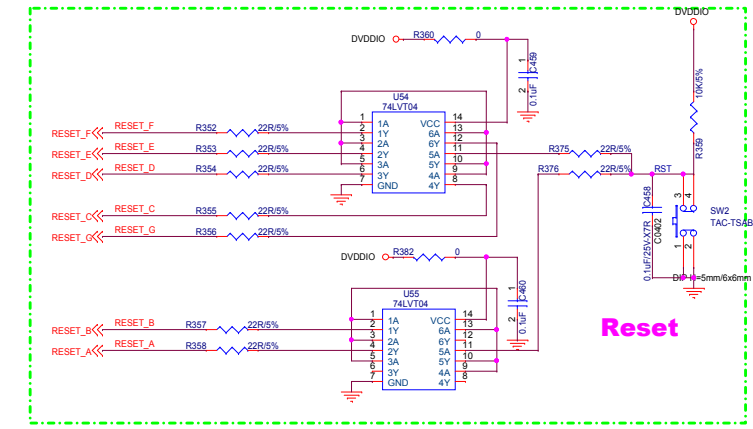
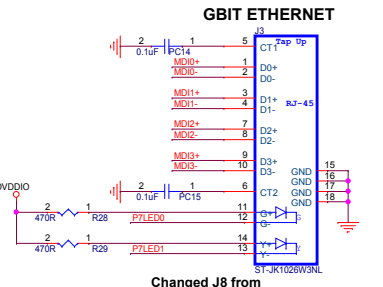
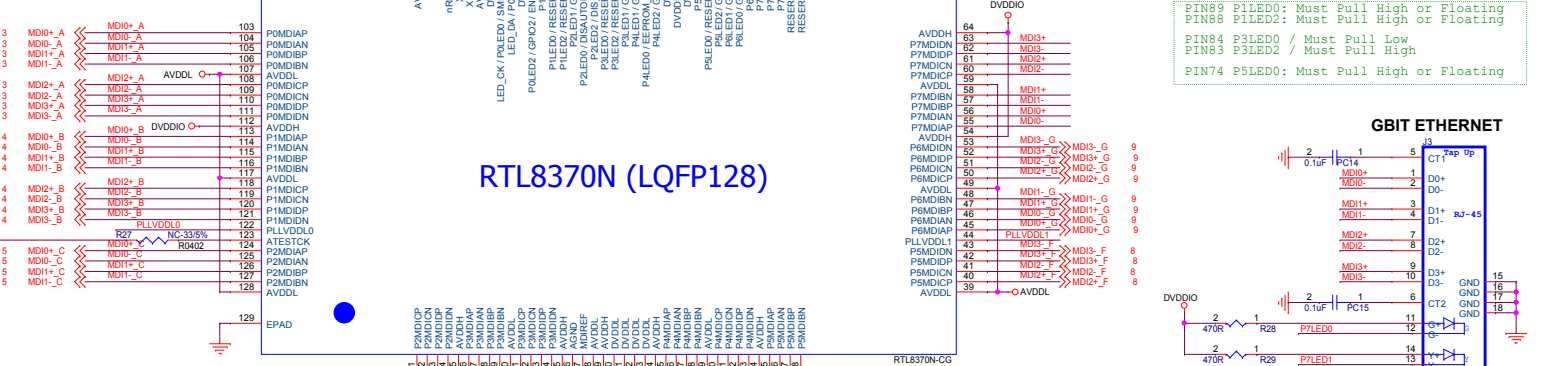
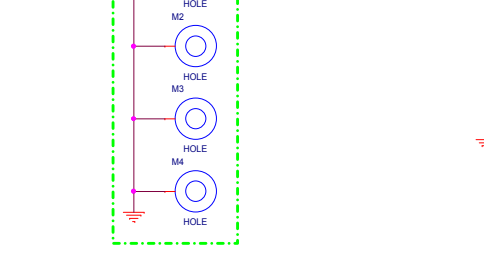
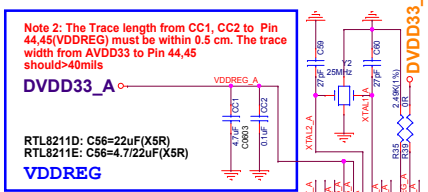
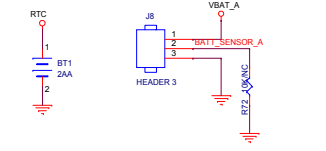
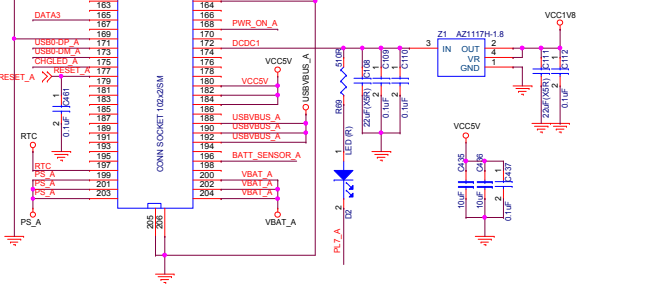
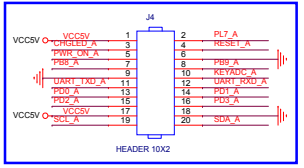
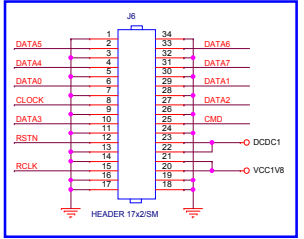
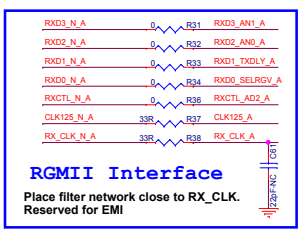
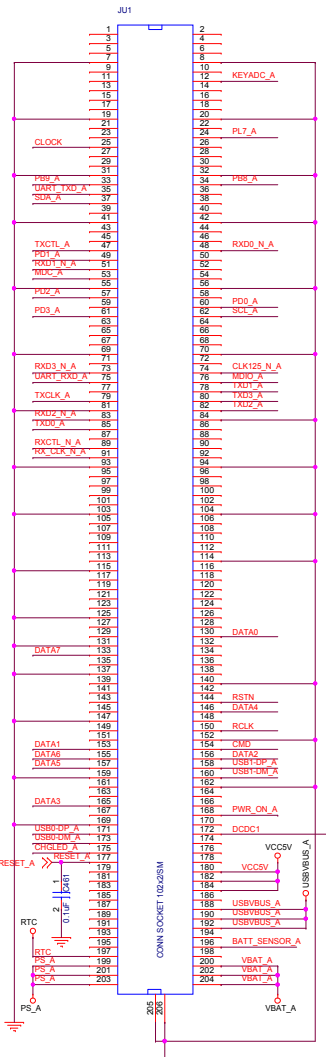


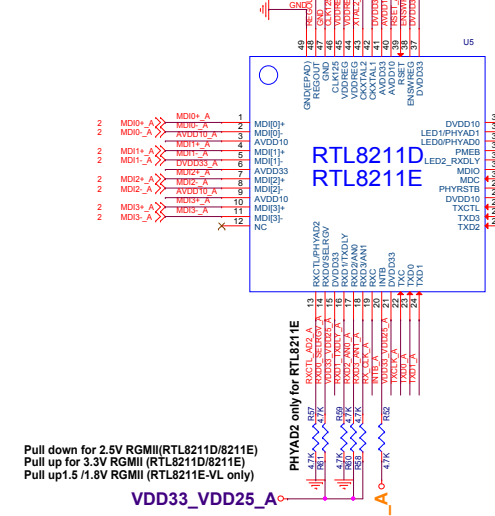
- PIN94 LED_CK / F0LED0 / SMI_SEL:
** Pull Up: EEPROM SMI interface.
Pull Down: MDIO slave interface.
- PIN91 GPIO2 / F0LED2 / EN_PHY:
** Pull Up: Enable embedded PHY
Pull Down: Disable embedded PHY
- PIN86 P2LED0 / DISAULOAD
** Pull Low: Enable Auto load EEPROM
Pull High: Disable Auto load EEPROM
- PIN85 P2LED2 / DIS 8051
** Pull Low: Enable 8051
** Pull High: Disable 8051
- PIN80 P4LED0 / EEPROM_MOD
** Pull Low: 24C02-24C16
Pull High: 24C32
- PIN89 P1LED0: Must Pull High or Floating
PIN88 P1LED2: Must Pull High or floating
PIN84 P3LED0 / Must Pull Low
PIN83 P3LED2 / Must Pull High
PIN74 P5LED0: Must Pull High or Floating



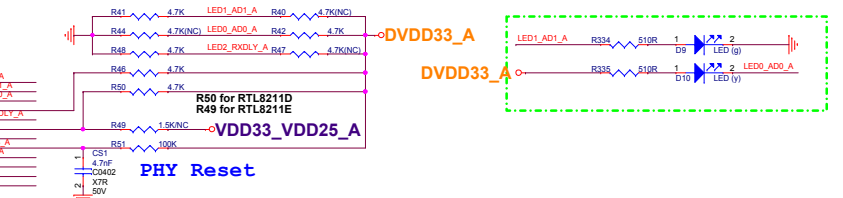
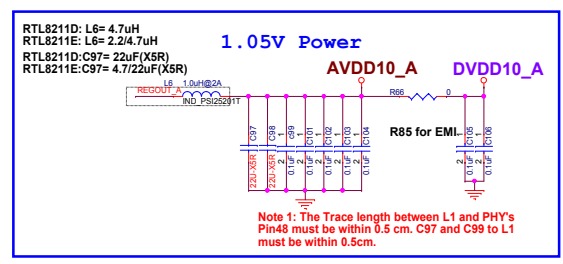
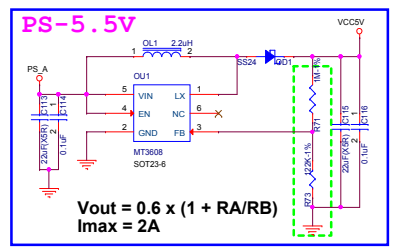
Changed J8 from HY91130A to ST-JK1026W3NL



Connect ENSWREG to DVDD33_A to enable Switching regulator or connect ENSWREG to GND to disable Switching regulator



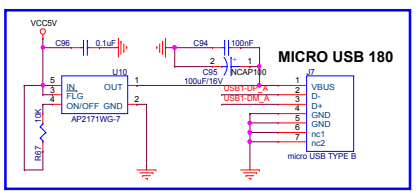
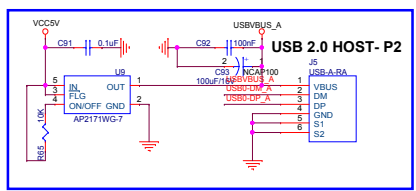
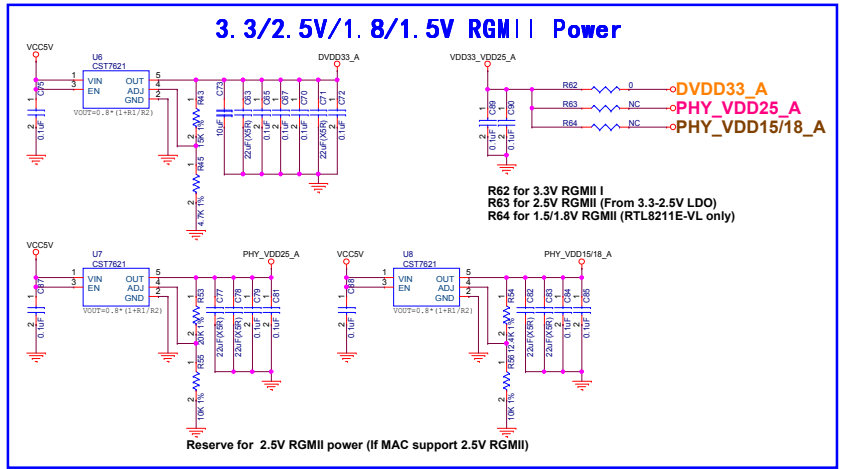
PHYAD2 only for RTL8211E
Pull down for 2.5V RGMII(RTL8211D/8211E)
Pull up for 3.3V RGMII (RTL8211D/8211E)
Pull up for 1.5/1.8V RGMII (RTL8211E-VL only)

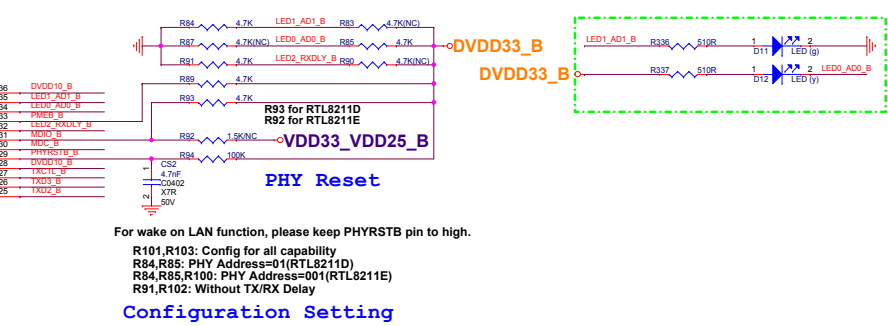
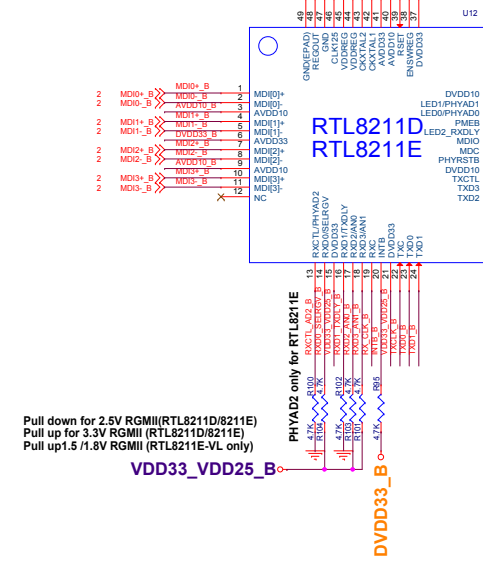
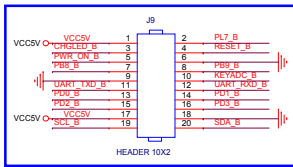
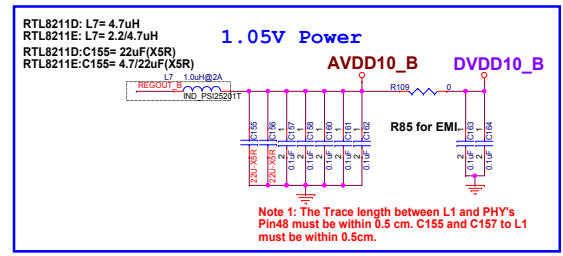
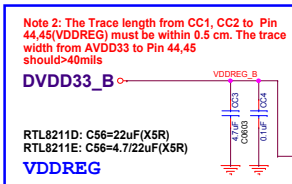
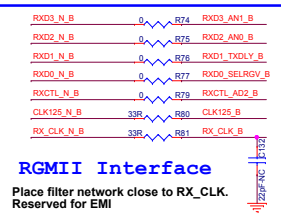
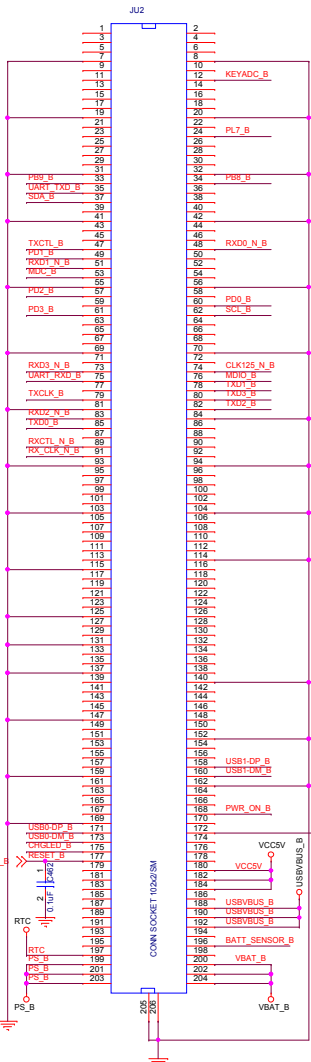


For wake on LAN function, please keep PHYRSTB pin to high.

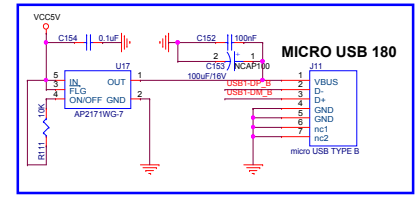
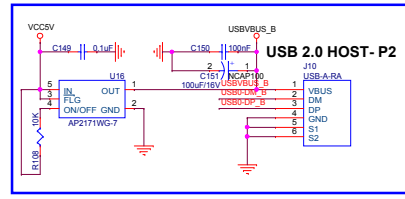
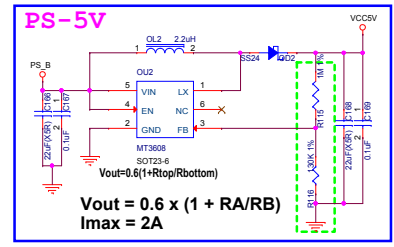
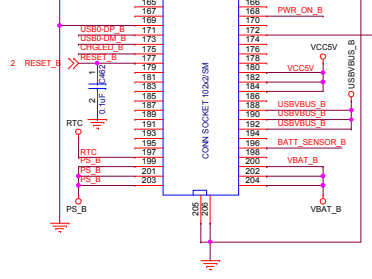
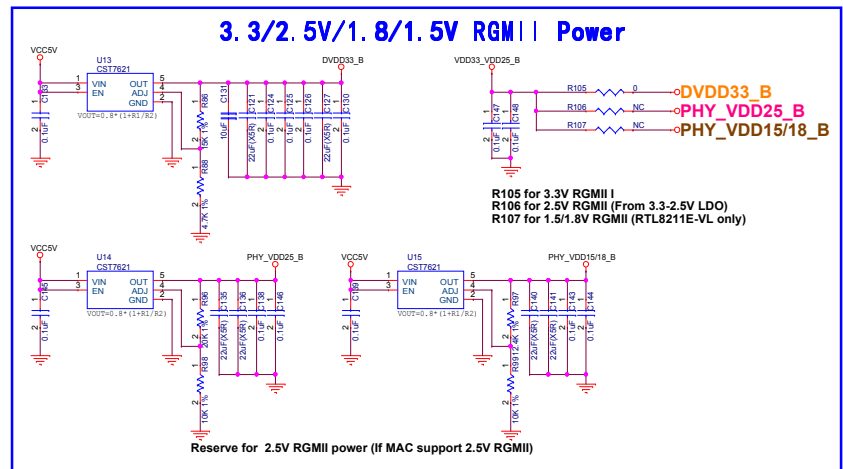
- R60,R58: Config for all capability
- R41,R42: PHY Address=01(RTL8211D)
- R41,R42,R57: PHY Address=001(RTL8211E)
- R59,R48: Without TX/RX Delay

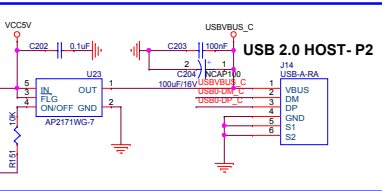
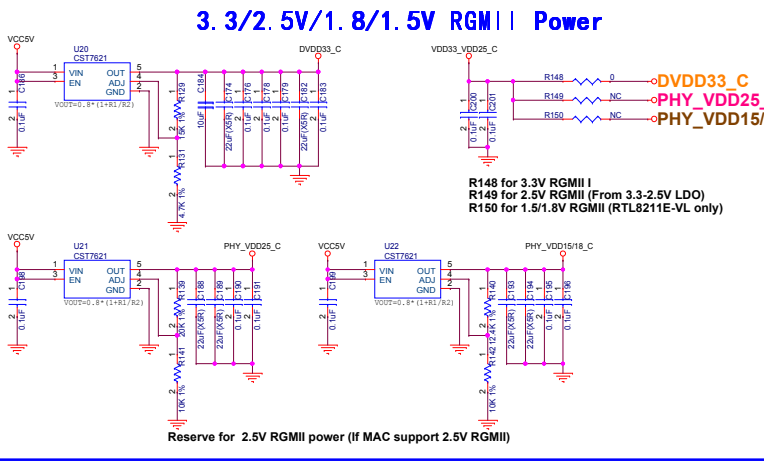
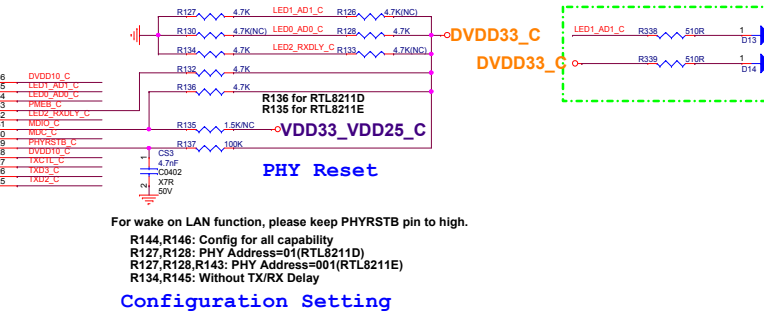
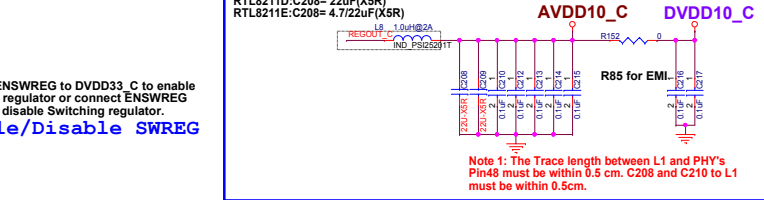
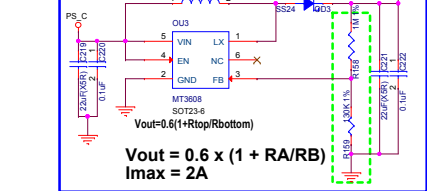
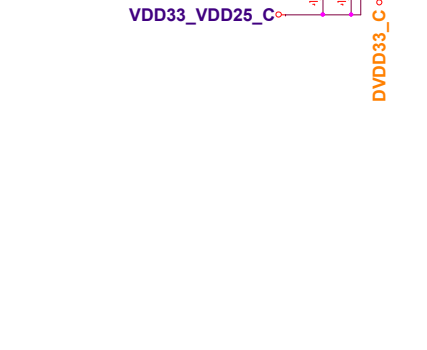
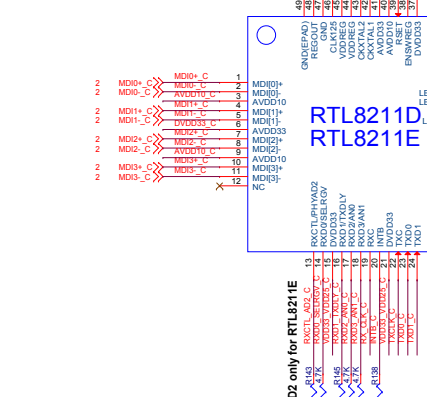
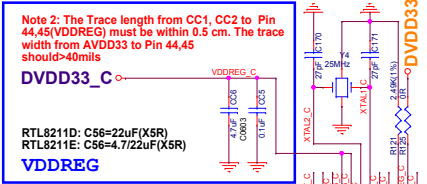
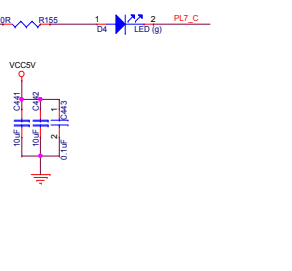
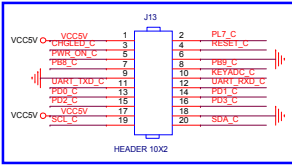
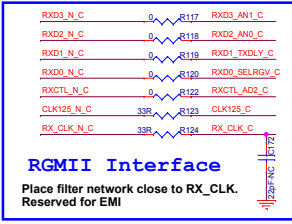
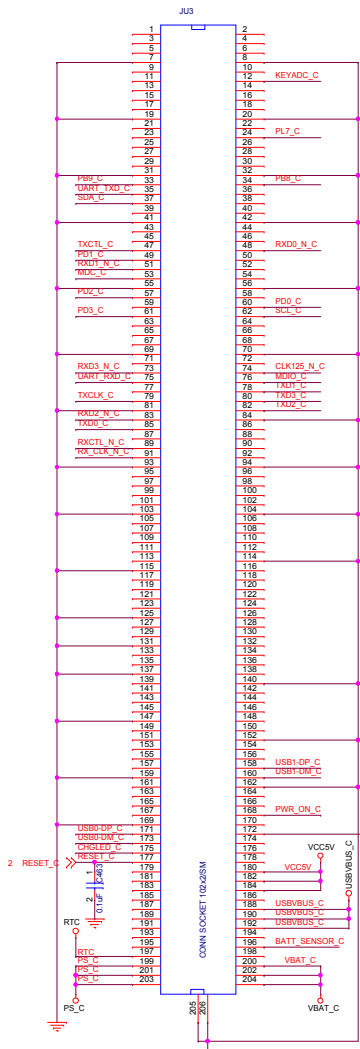
Configuration Setting

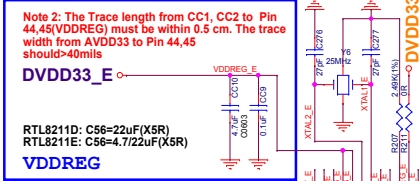
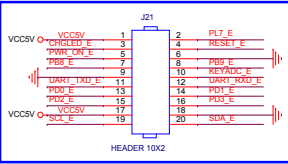
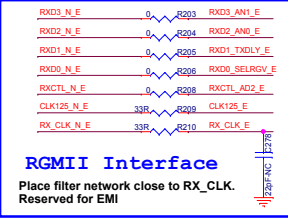
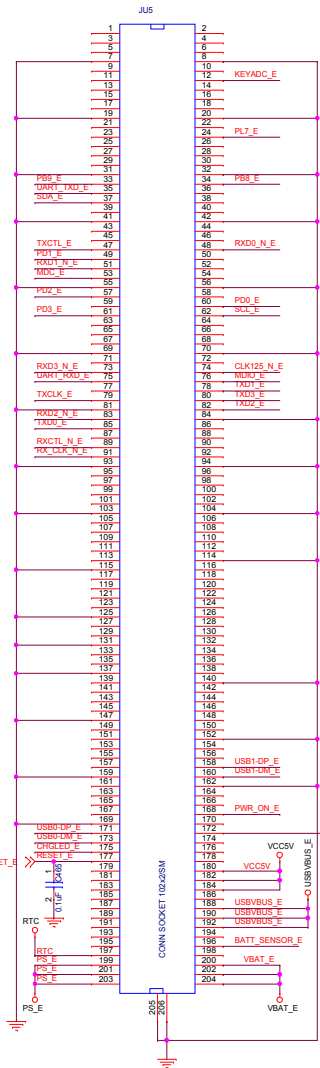




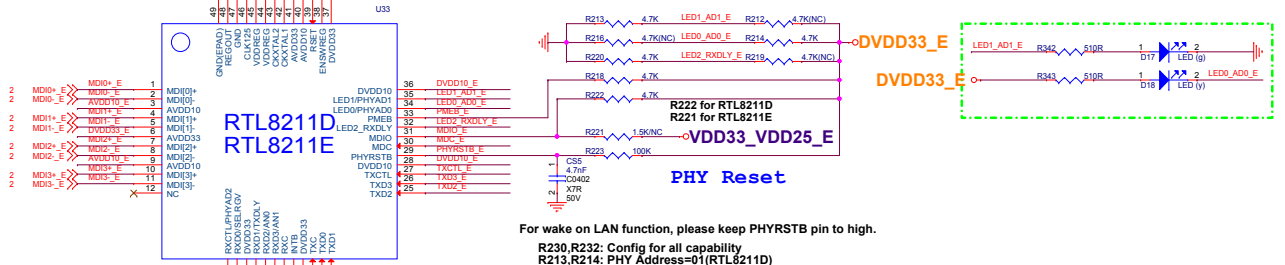
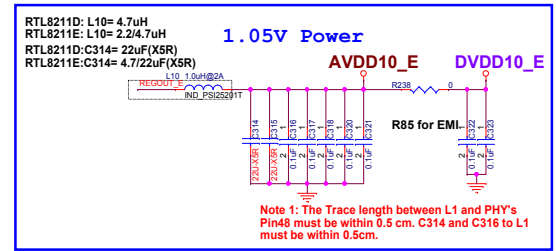
Pull down for 2.5V RGMII(RTL8211D/8211E)
Pull up for 3.3V RGMII (RTL8211D/8211E)
Pull up 1.5 /1.8V RGMII (RTL8211E-VL only)





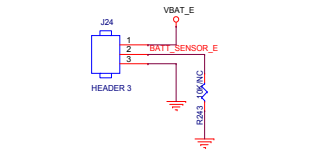
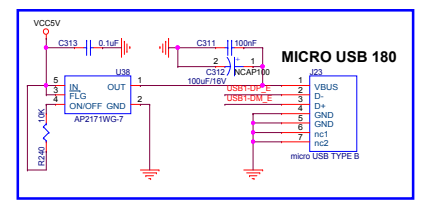
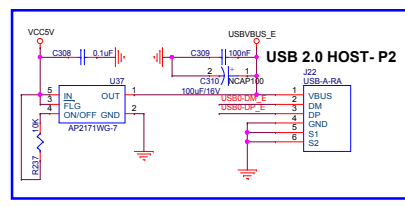
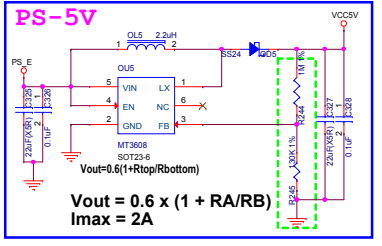
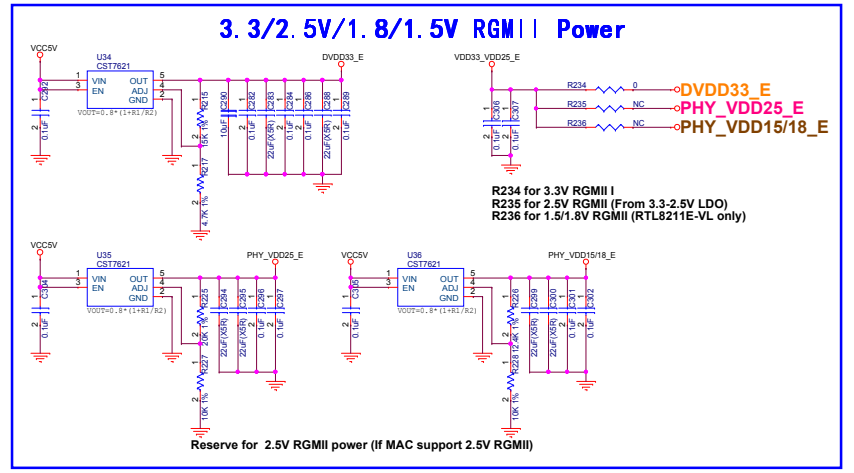


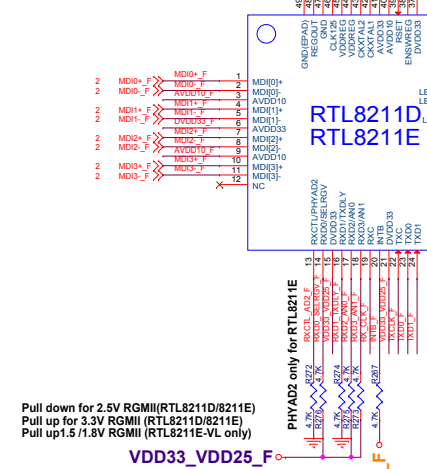
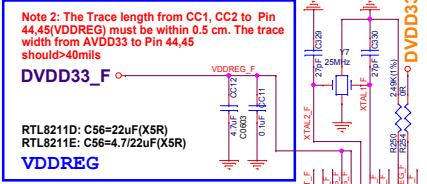
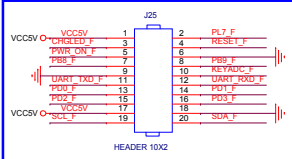
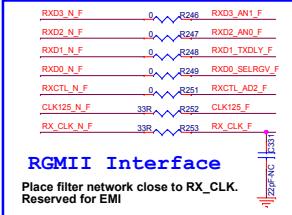
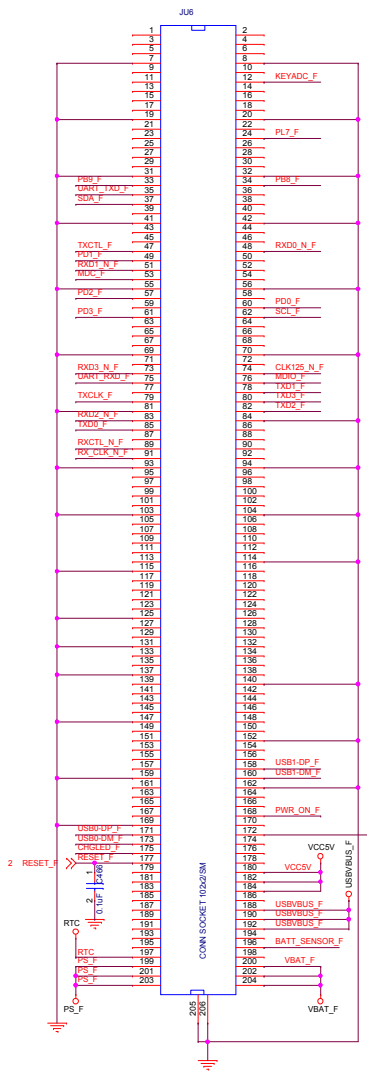
Connect ENSWREG to DVDD33_E to enable Switching regulator or connect ENSWREG to GND to disable Switching regulator.
Enable/Disable SWREG



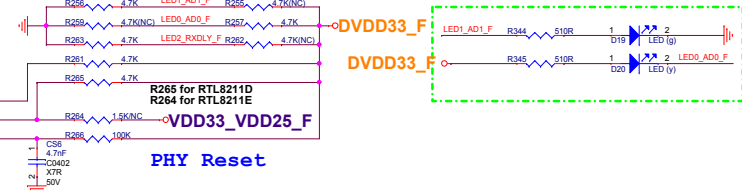
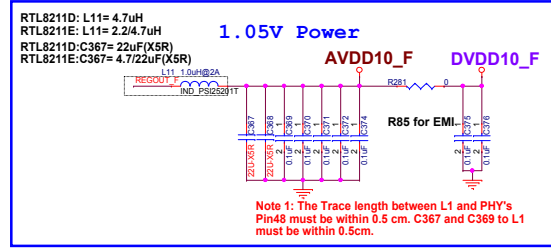
Configuration Setting

Pull up for 2.5V RGMII(RTL8211D/8211E)
Pull up for 3.3V RGMII (RTL8211D/8211E)
Pull up 1.5/1.8V RGMII (RTL8211E-VL only)





Connect ENSWREG to DVDD33_F to enable Switching regulator or connect ENSWREG to GND to disable Switching regulator.
Enable/Disable SWREG



For wake on LAN function, please keep PHYRSTB pin to high.

- R273, R275: Config for all capability
- R256, R257: PHY Address=01(RTL8211D)
- R256, R257, R272: PHY Address=001(RTL8211E)
- R263, R274: Without TX/RX Delay

Configuration Setting

