

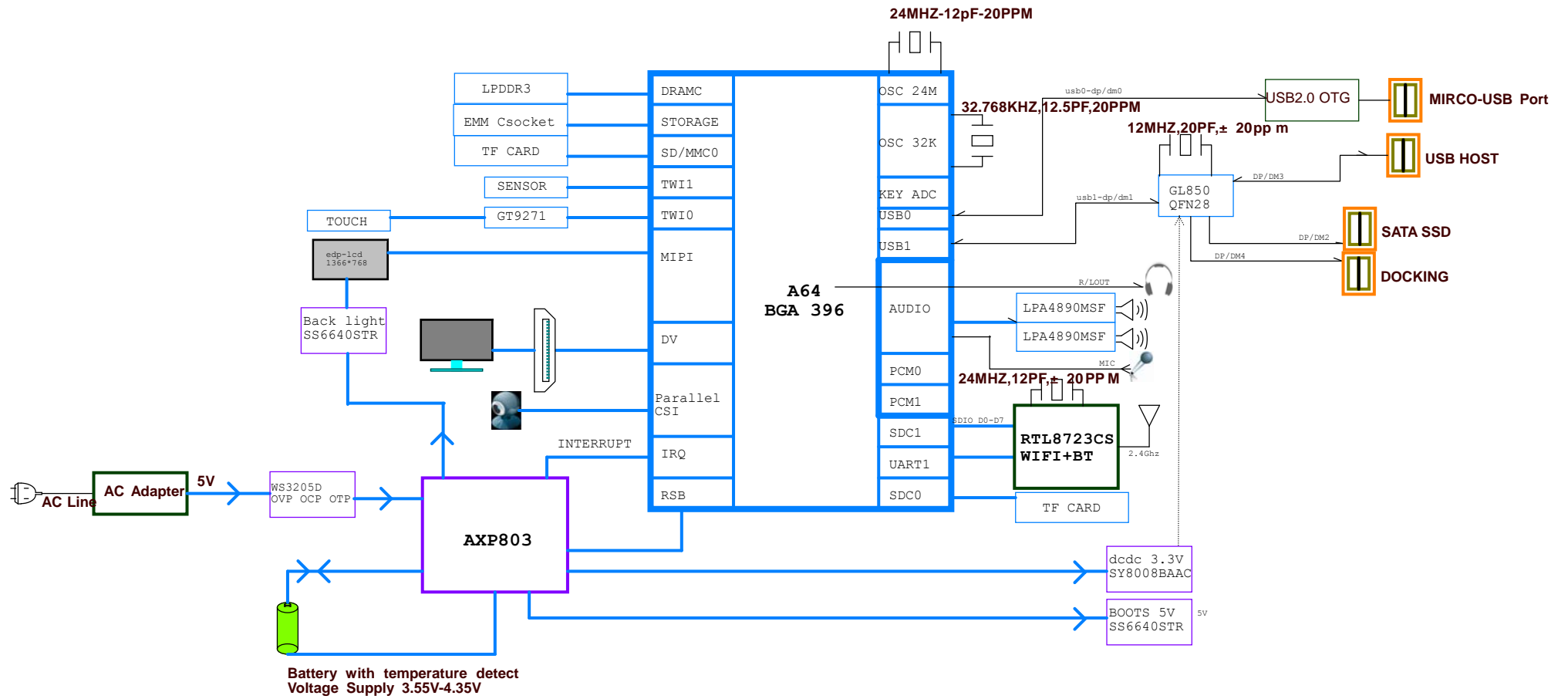
VERSION HISTORY

Index:

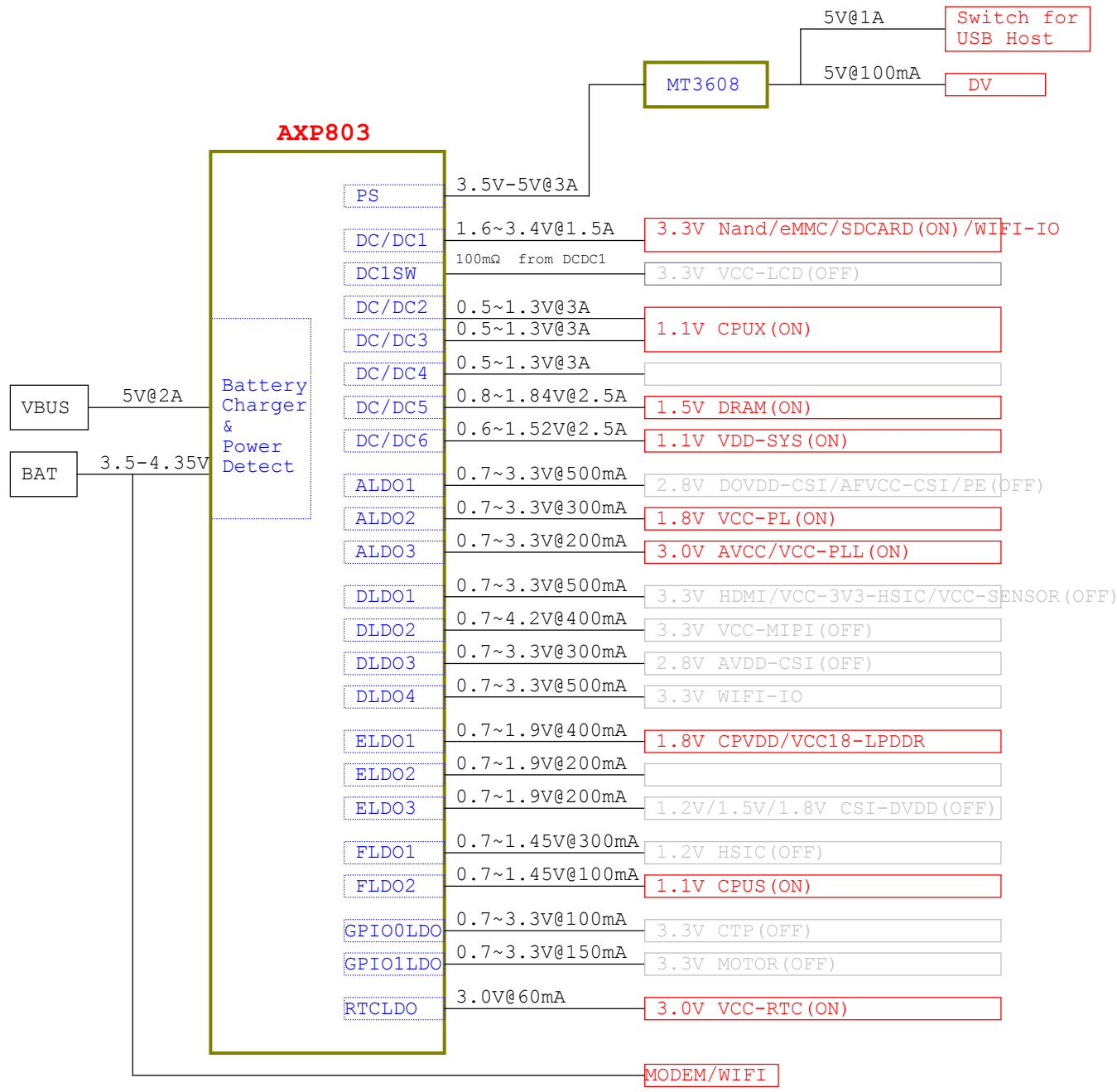
- 01 VERSION HISTORY
- 02 BLOCK DIAGRAM
- 03 POWER TREE
- 04 LPDDR3 FGBA178
- 05 CPU
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- 07 FLASH
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Revision	Description	Date	Drawn	Checked	Approved
Ver 1.2	Release version	2019-11-25			

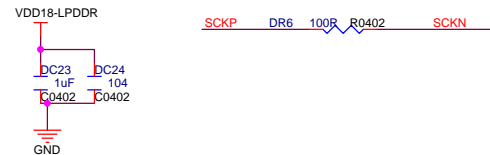
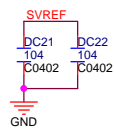
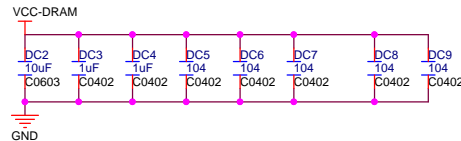
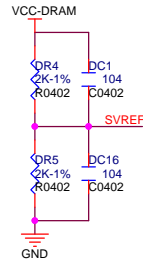
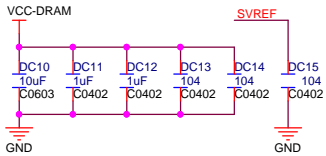
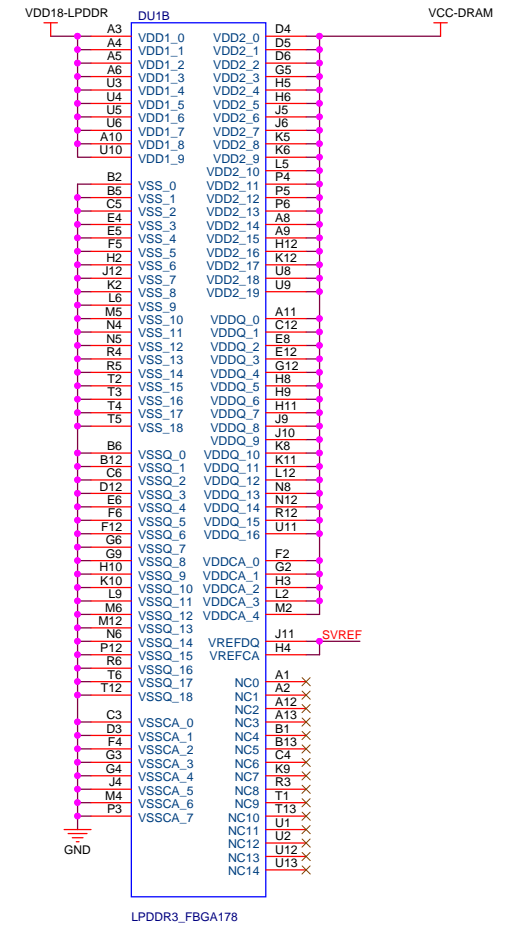
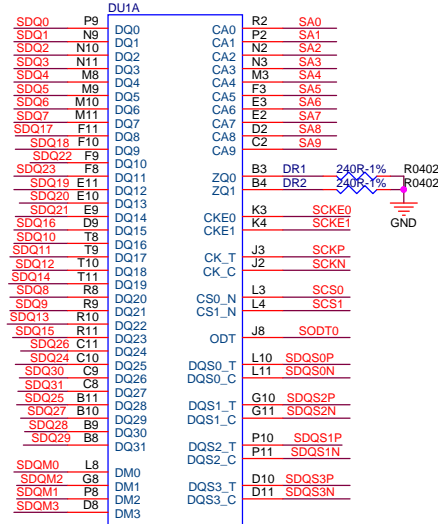
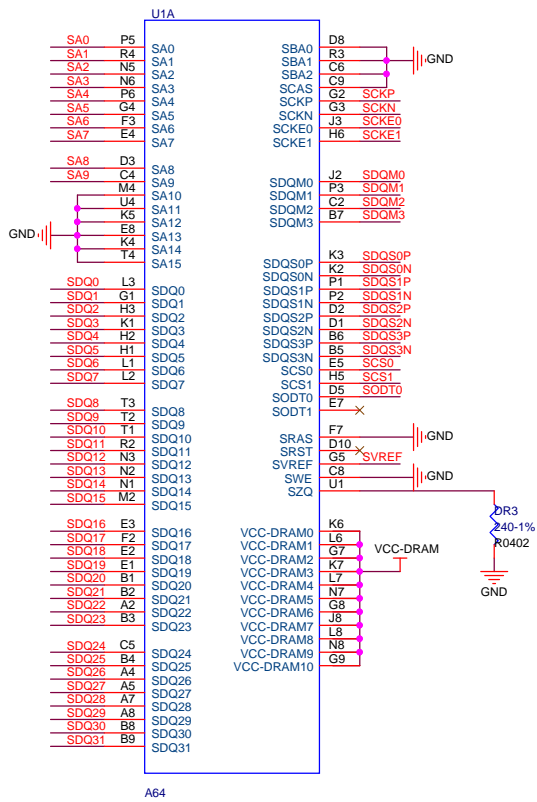
BLOCK DIAGRAM



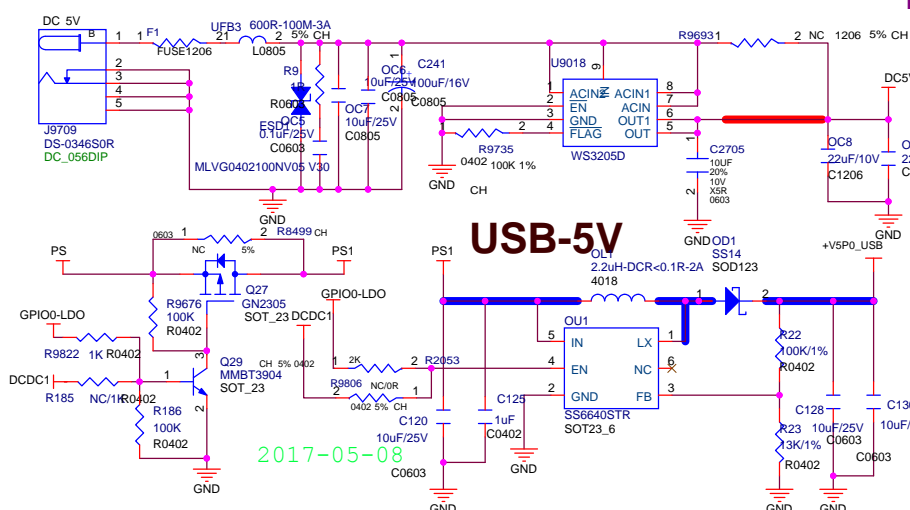
POWER TREE



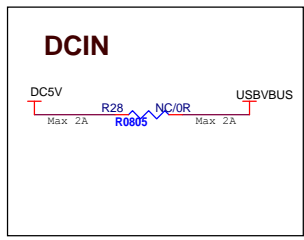
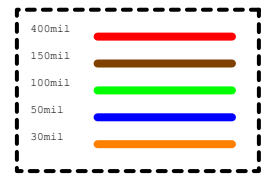
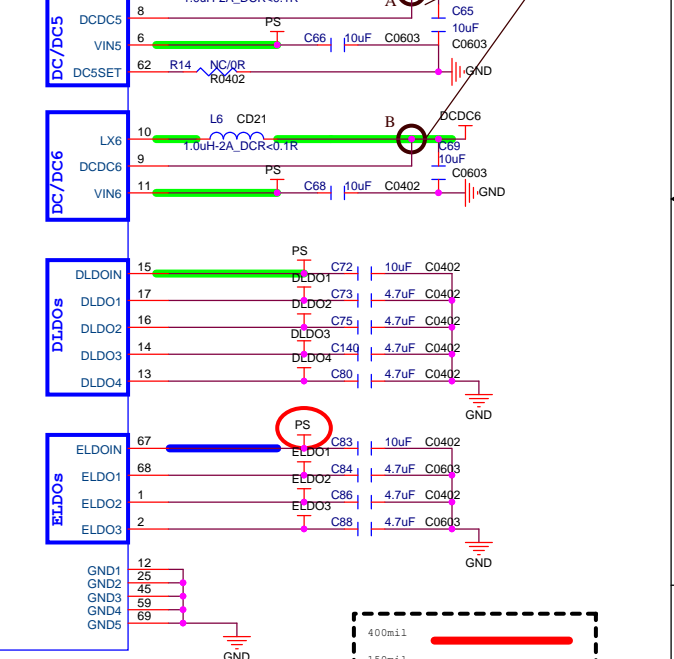
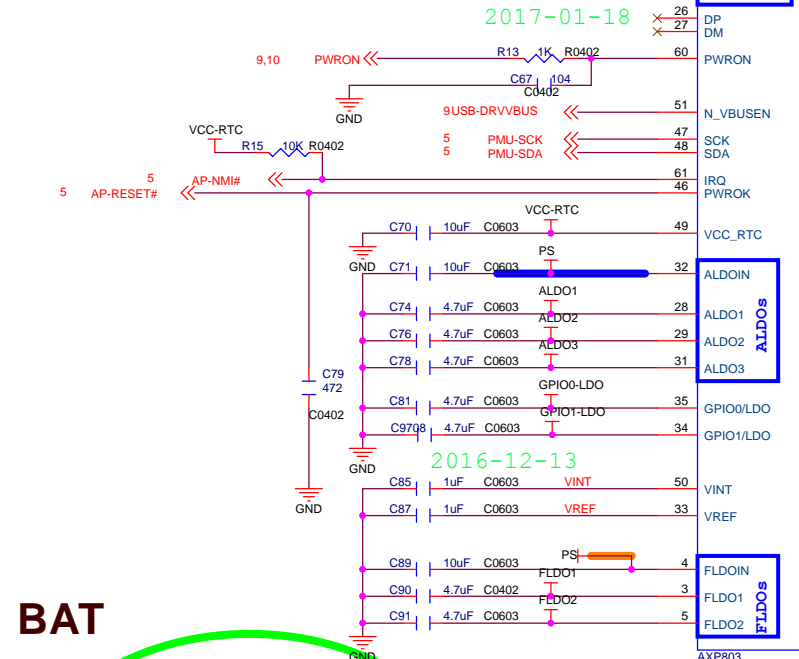
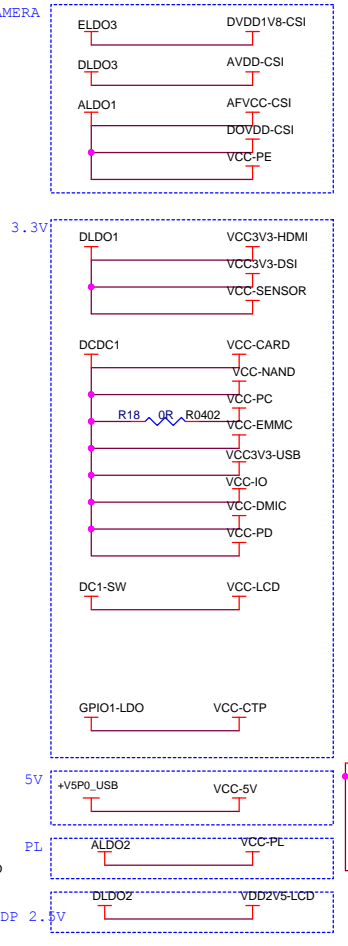
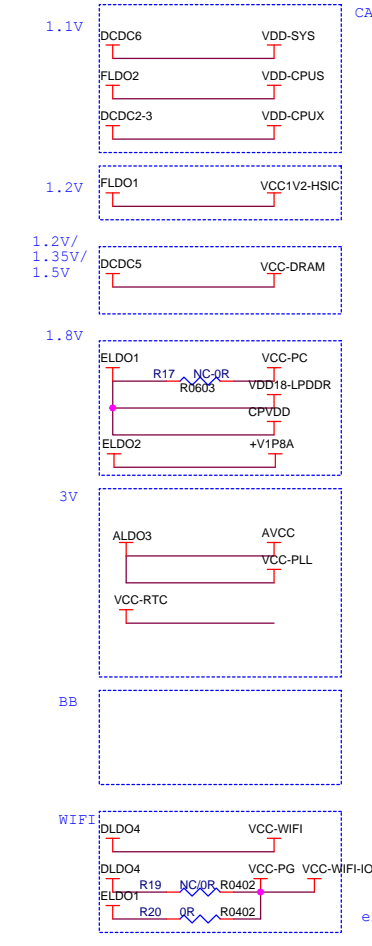
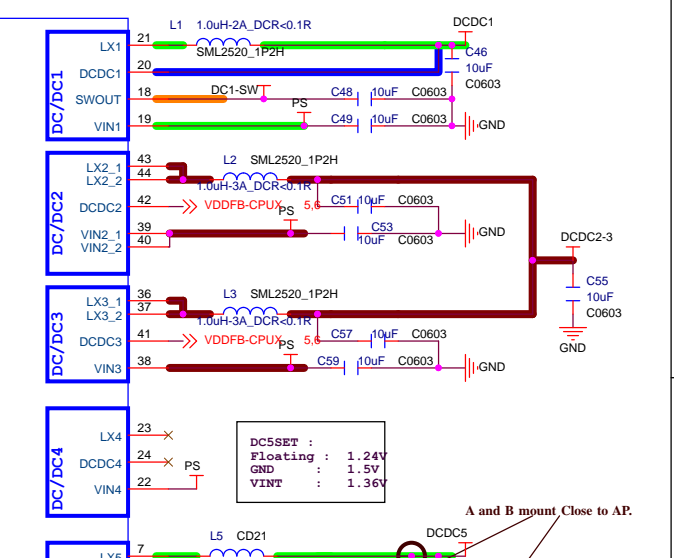
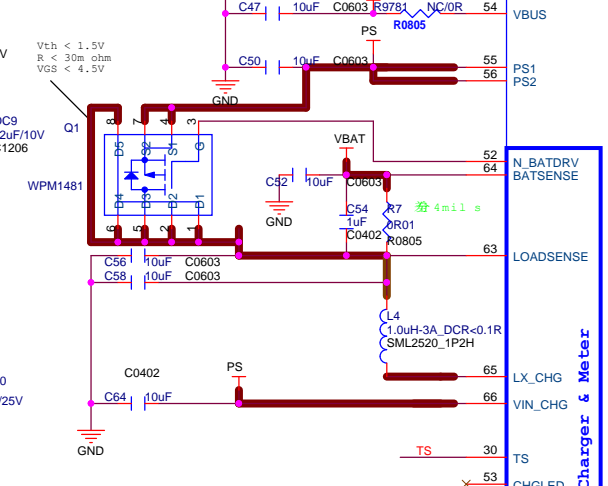
LPDDR3



Power From AC adapter

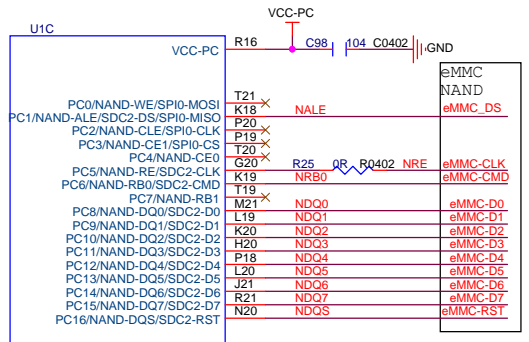


PMIC

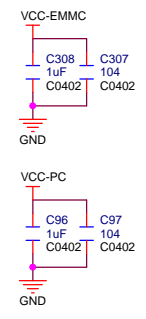
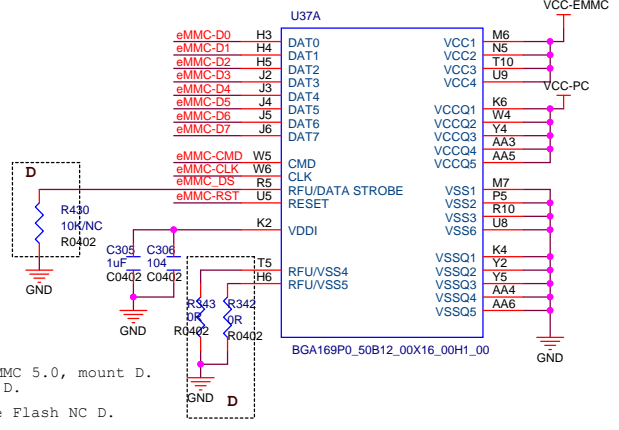
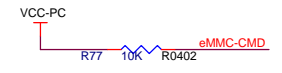
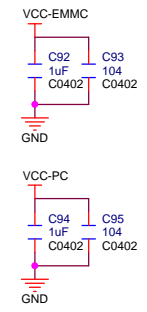
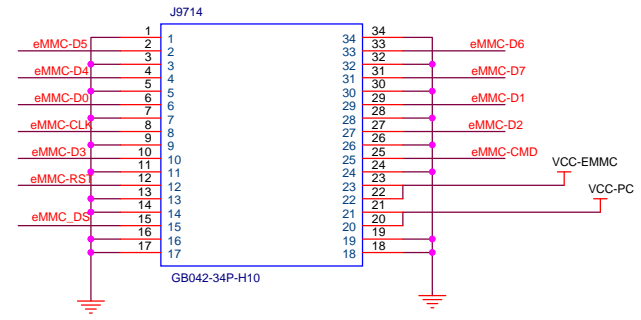
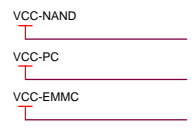


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NAND/eMMC



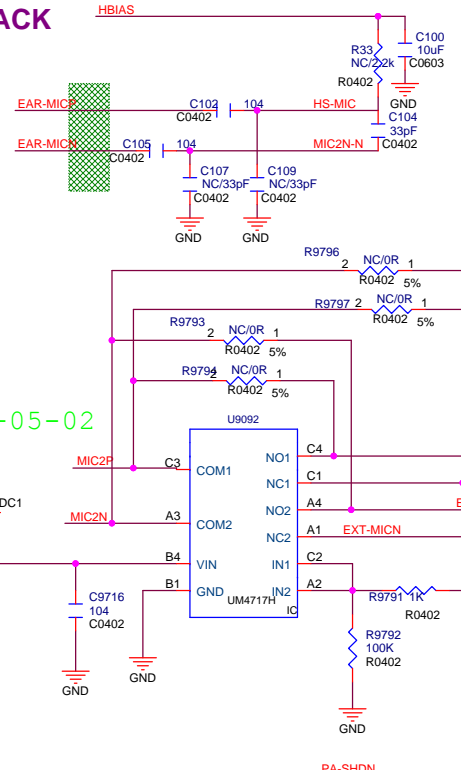
A64



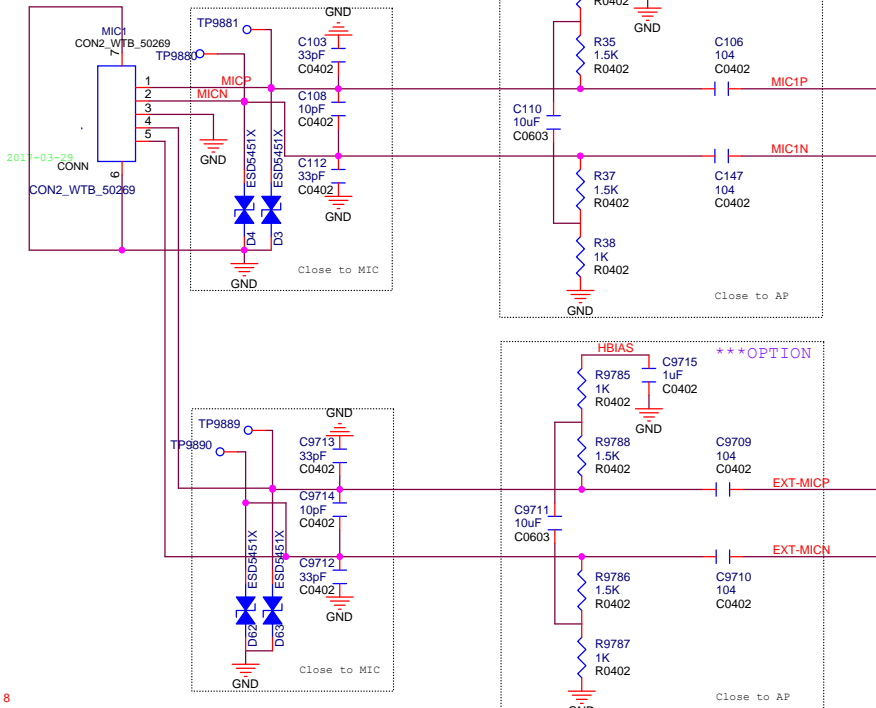
If use eMMC 5.0, mount D.
else, NC D.
Or if use Flash NC D.

AUDIO

HP_JACK

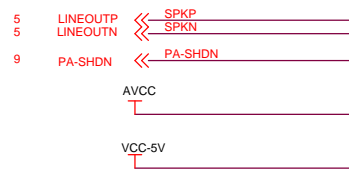


ECM AMIC

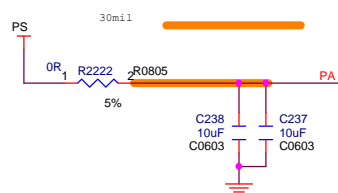
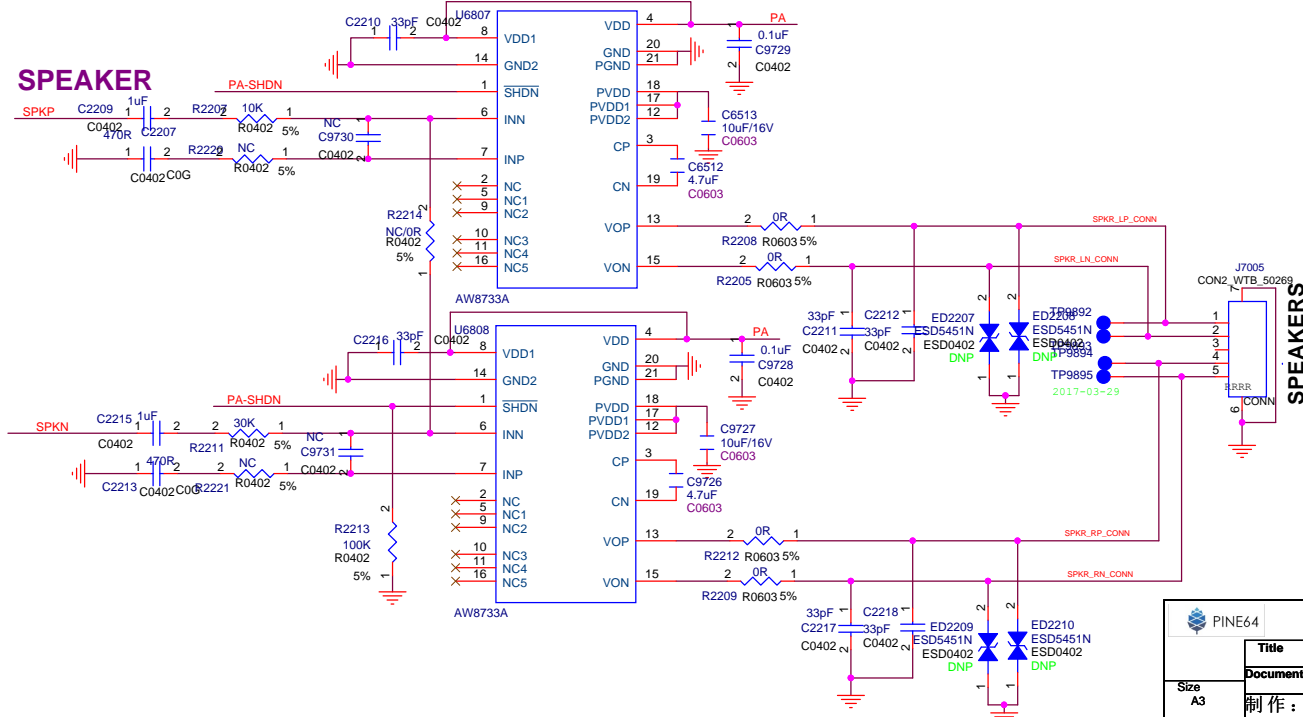


2017-05-02

2016-12-20

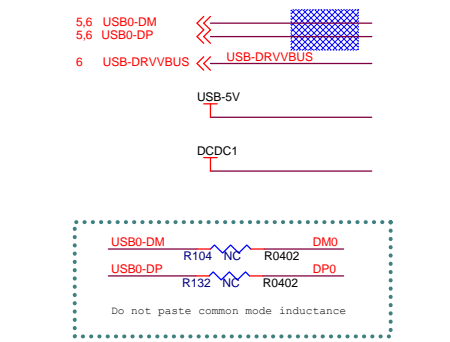
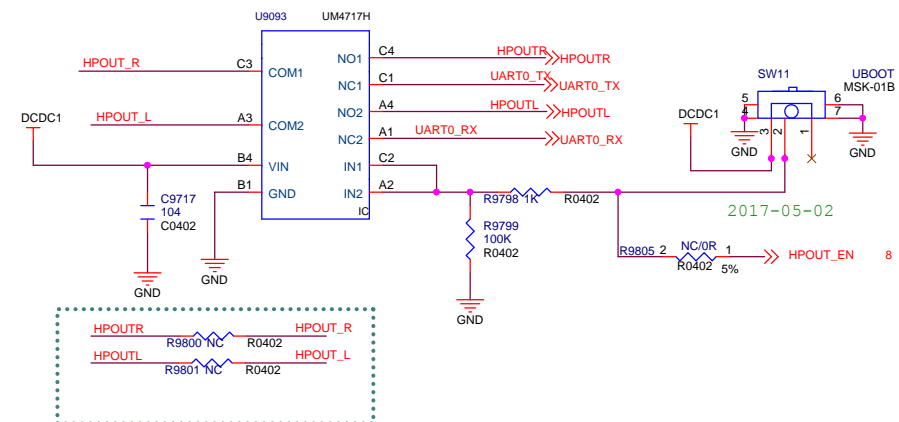
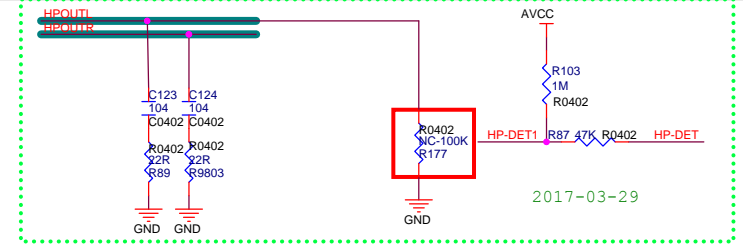
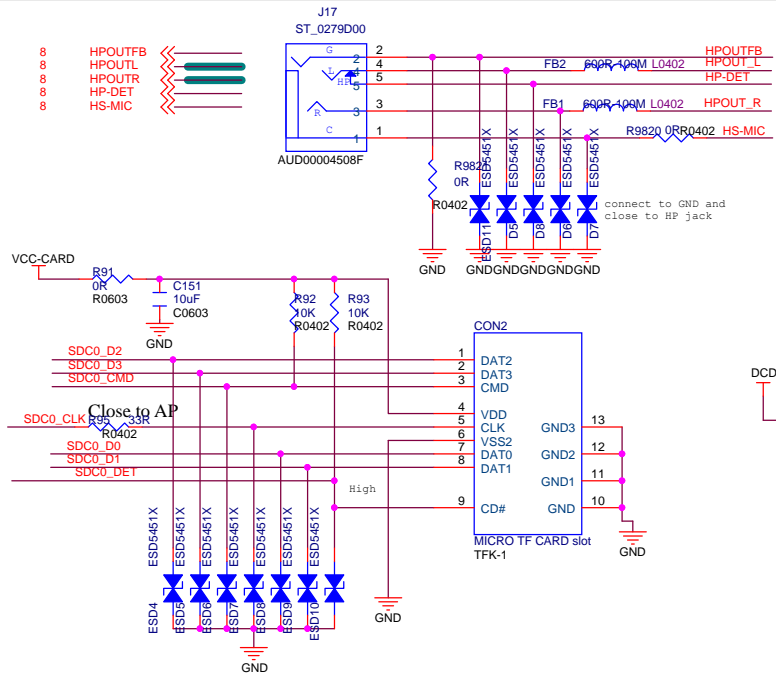
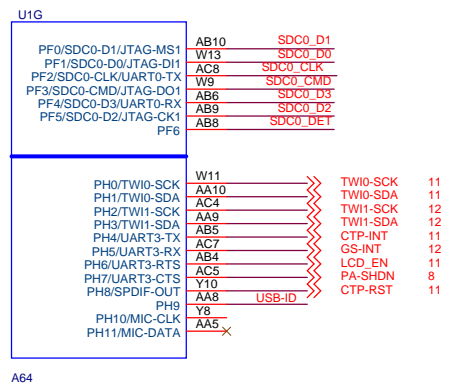


SPEAKER

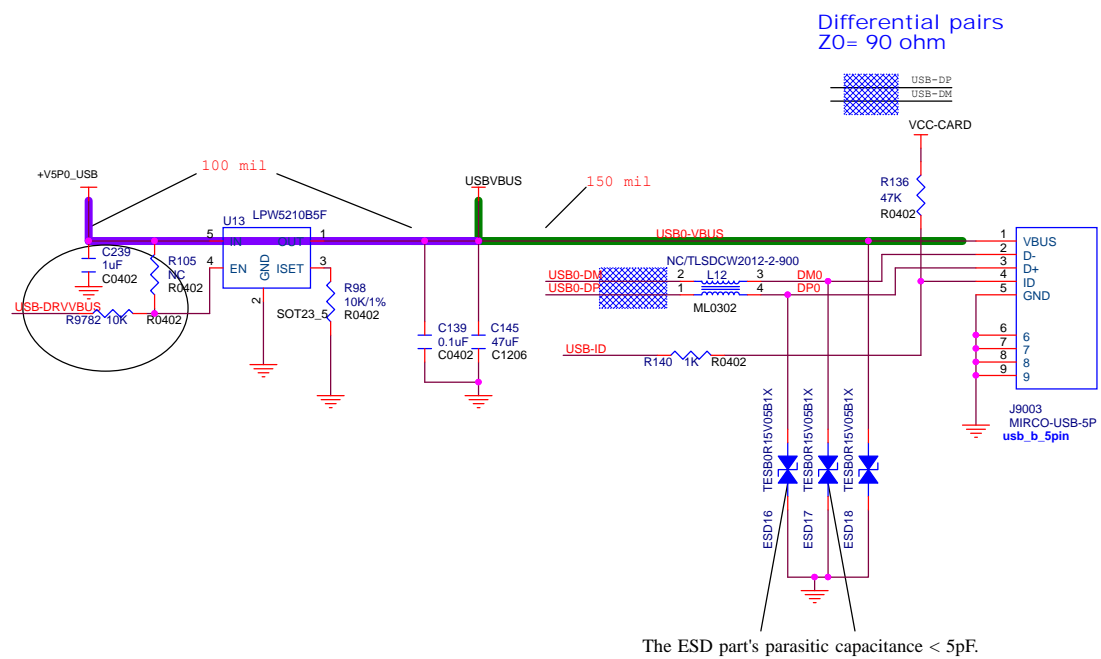


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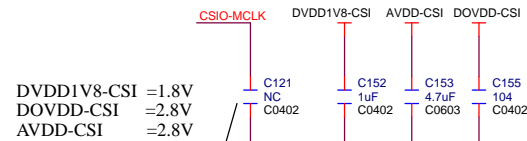
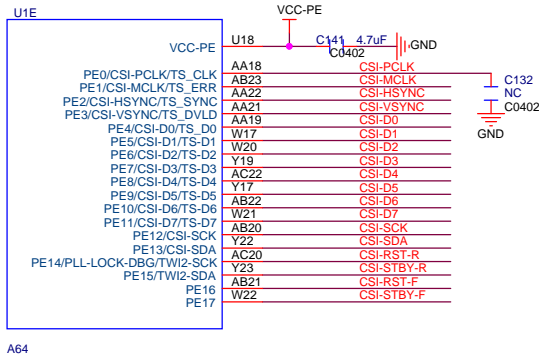
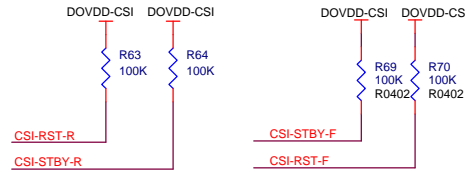
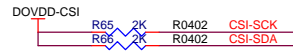
T-CADD/USB



USB

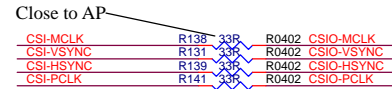
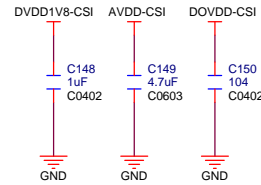


CAMERA



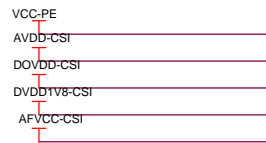
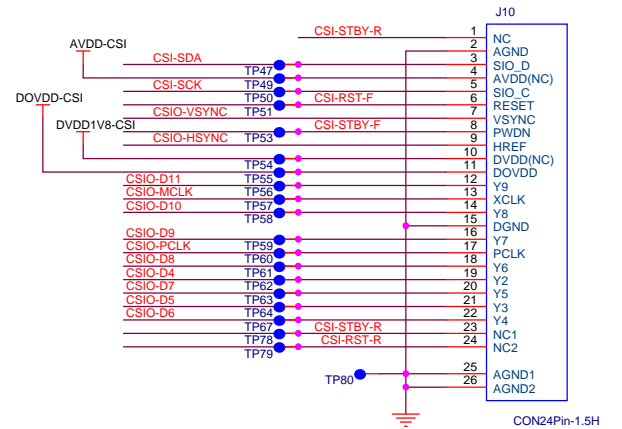
DVDD1V8-CSI =1.8V
DOVDD-CSI =2.8V
AVDD-CSI =2.8V

Close to camera.



Close to AP

Close to Camera



D

D

C

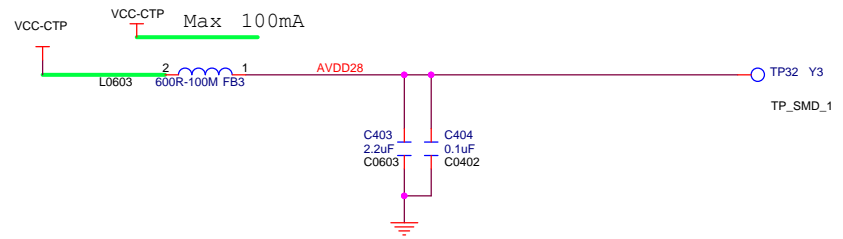
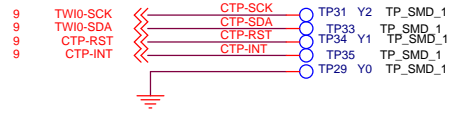
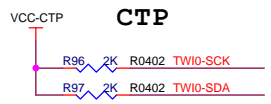
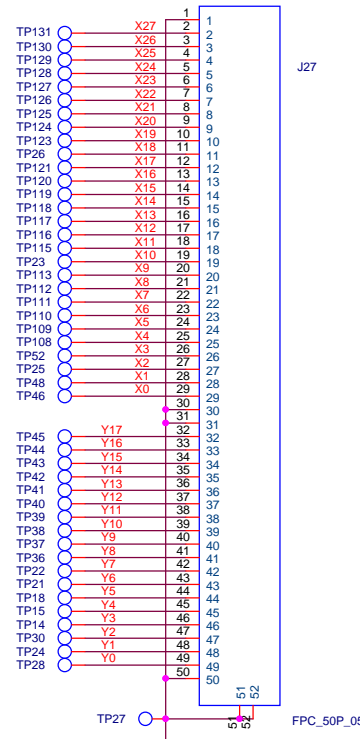
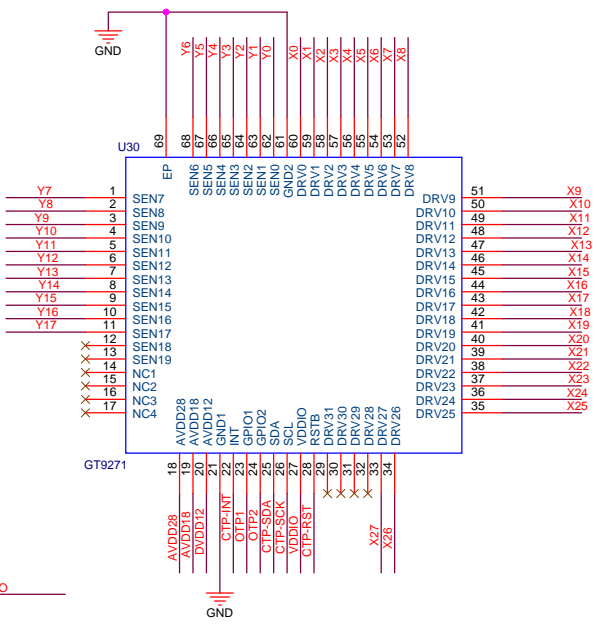
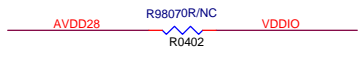
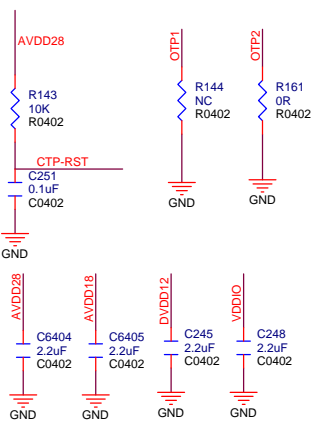
C

B

B

A

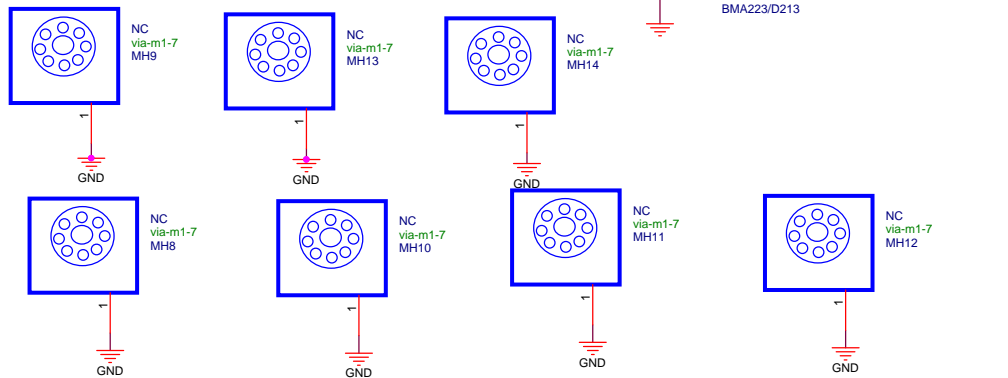
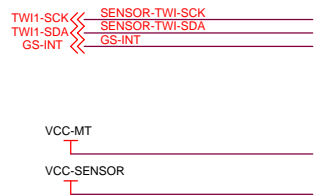
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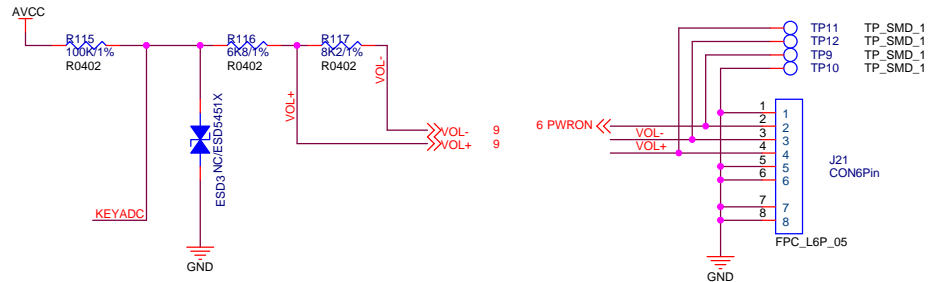
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SENSORs/MT/KEY

SENSOR

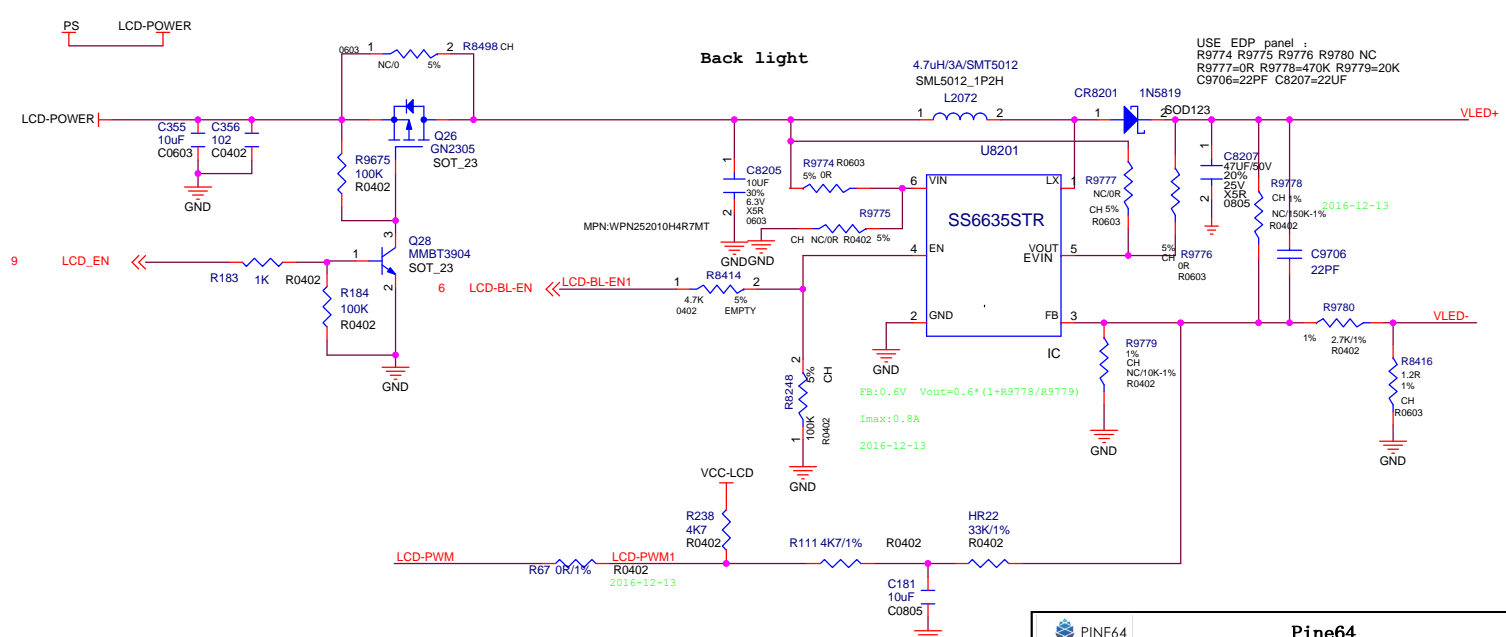
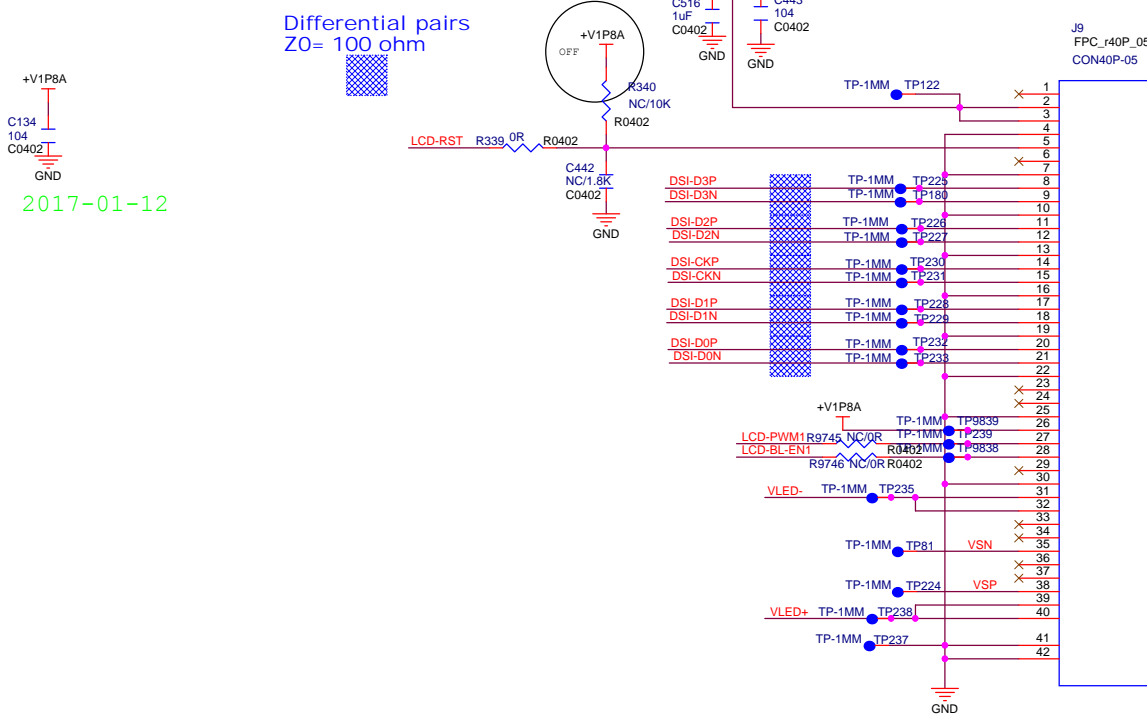
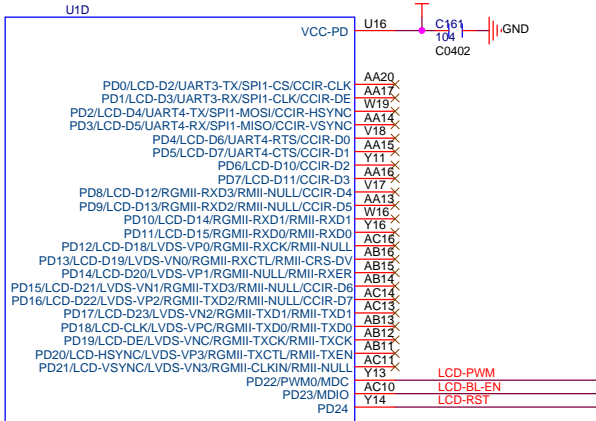


KEY



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MIPI

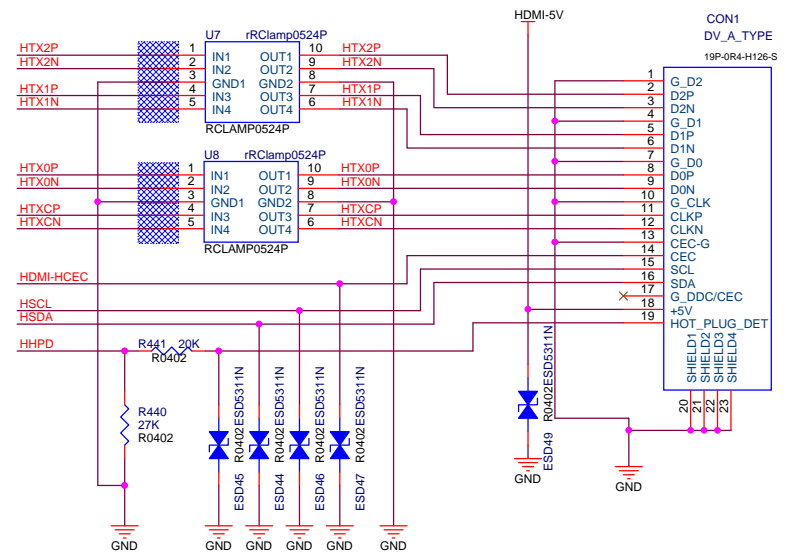
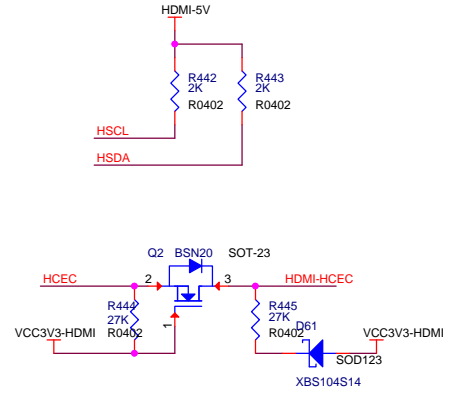


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DIGITAL VIDEO

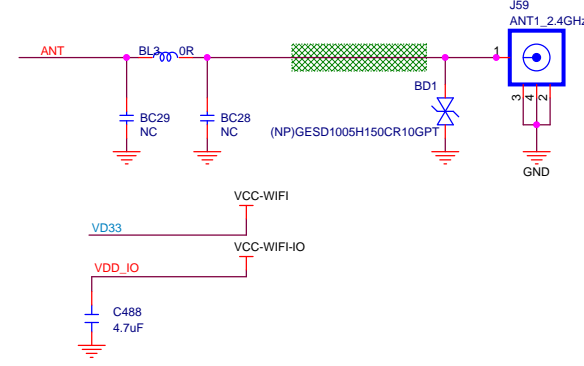
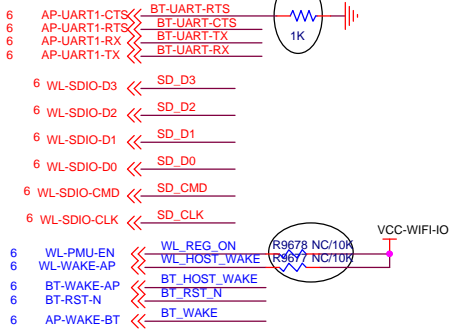
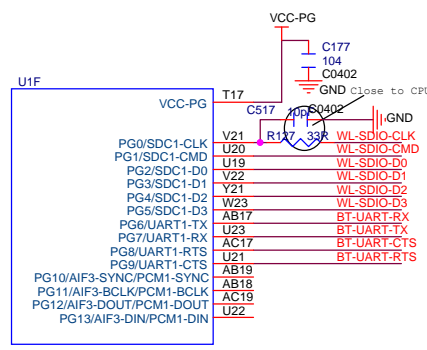


Differential pairs
Z0 = 100 ohm



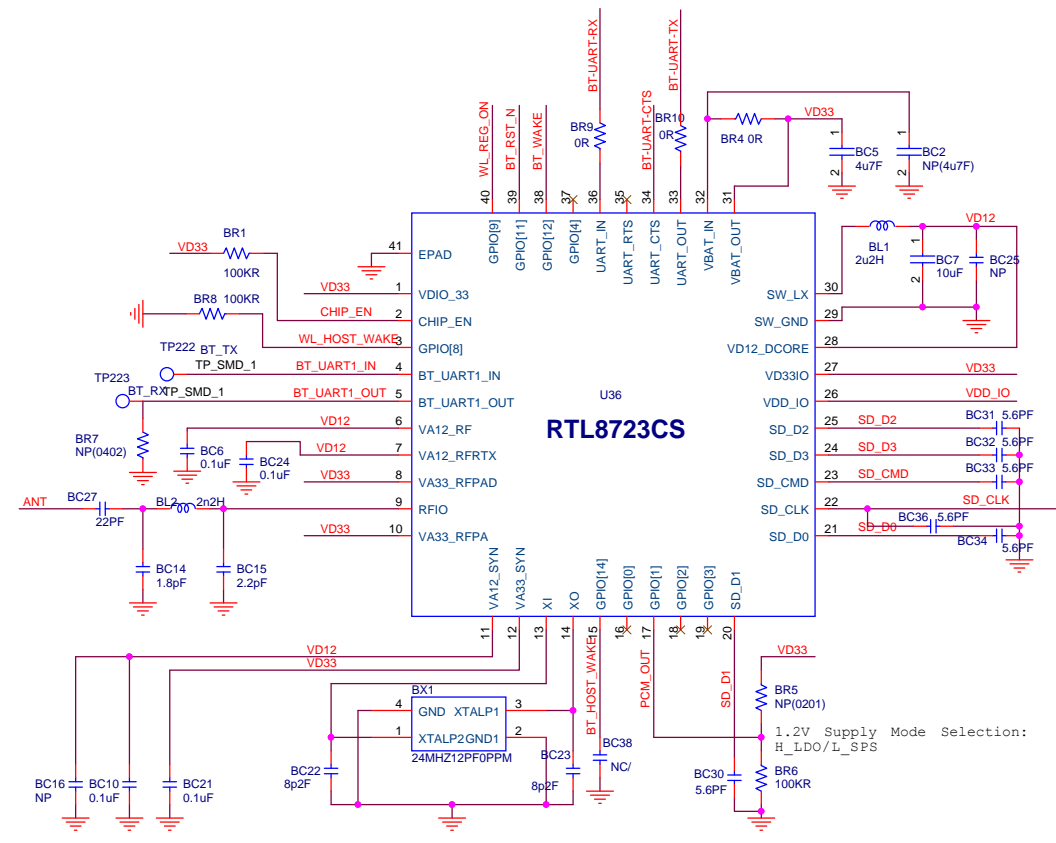
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RTL8703AS/8723BS-COB



A64

VCC-WIFI-HO



GPIO[8]/EXT_WOL/WL_WAKE_HST/WL_LED/BT_LED/ANTSEL2/ SD_OB_INT
 BT_UART1_IN/BT_3DD_SEL/BT_GPIO/BT_LED/ ANTSEL1/ANTSEL1
 BT_UART1_OUT/BT_3DD_SYNC/BT_GPIO/(WL_WAKE_HST)/TEST_MD_SEL/ANTSEL0
 GPIO[9]/WL_DISn/EXT_WOLO
 GPIO[11]/BT_DISn/ BT_EXT_WOL
 GPIO[12]/HST_WAKE_DEV/LTE_COEX/FM_I2S_SDOUT/WPS
 GPIO[4]/EEPROM_SEL/WL_LTE_UART_OUT/BT_GPIO(BT_WAKE_HST)/FM_I2S_WS/FM_I2C_SDA
 GPIO[14]/DEV_WAKE_HST/(WL_WAKE_HST)
 GPIO[0]/ICFG0/BT_GPIO/PCM_IN

Supply Voltage Options

Part	VBAT==3.3V	VBAT=3.3~5.5V (Battery)
BR4	0R	NP
BR1	100KR	NP(CHIP_EN: Off Module Pull High)
BC2	NP	4.7uF

PINE64 Pine64

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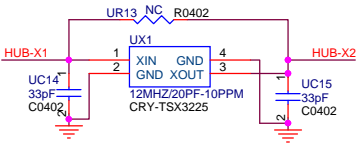
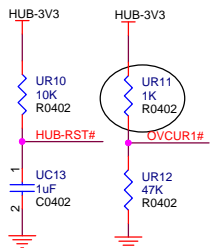
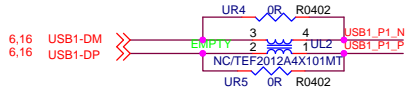
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Size A3 制作: 审核: Rev V1.2

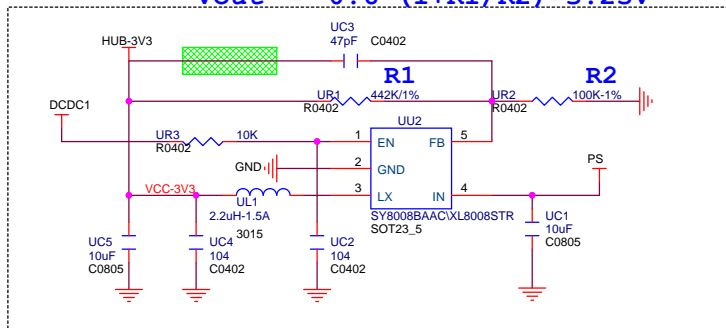
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USB HUB

Differential pairs
 $Z_0 = 90 \text{ ohm} \pm 5 \text{ ohm}$



VCC-3V3
 $V_{out} = 0.6 * (1 + R1/R2) = 3.25V$

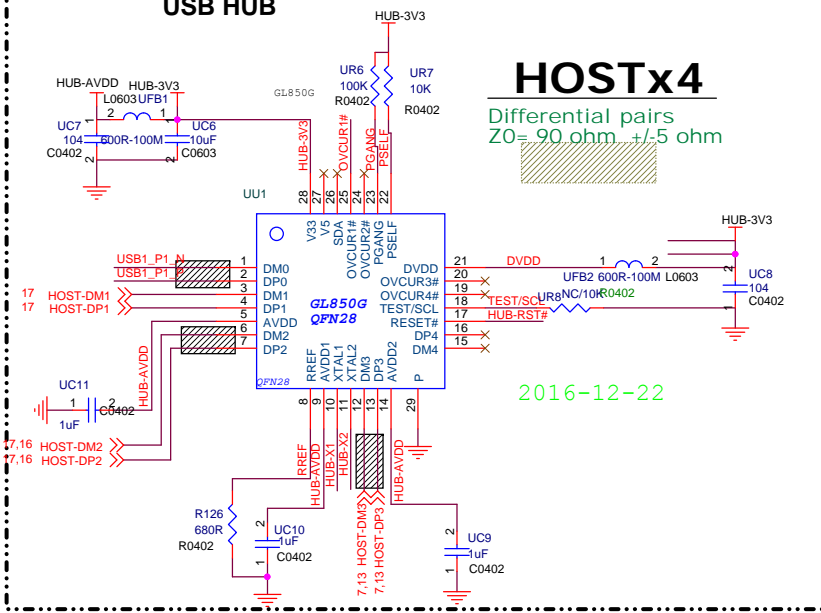


USB_HUB_USB_CONN

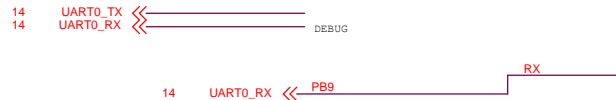
USB HUB

HOSTx4

Differential pairs
 $Z_0 = 90 \text{ ohm} \pm 5 \text{ ohm}$

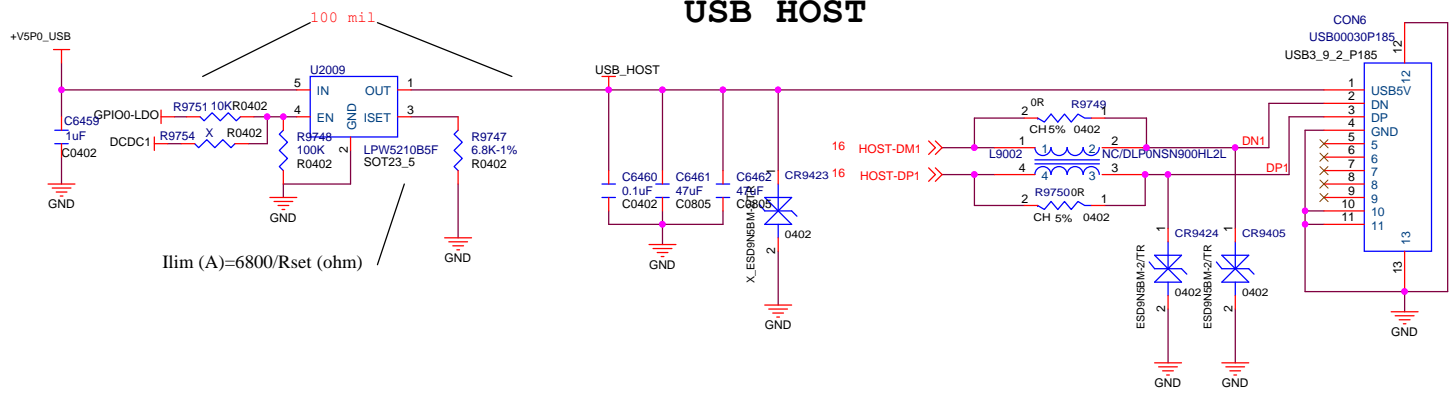


2016-12-22

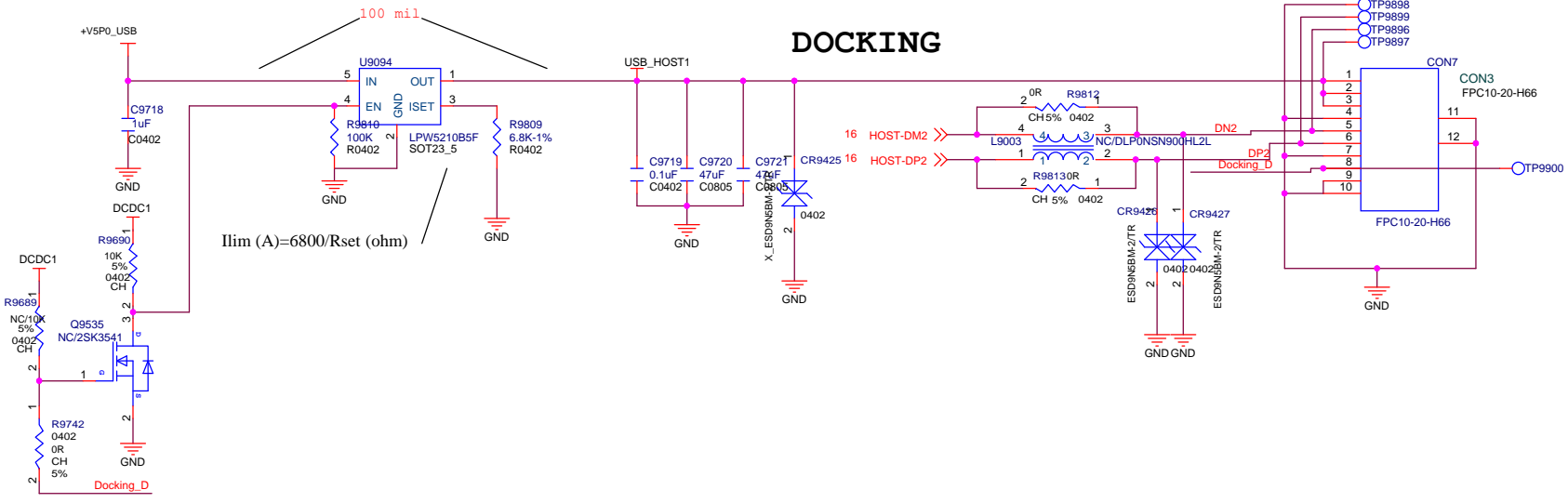


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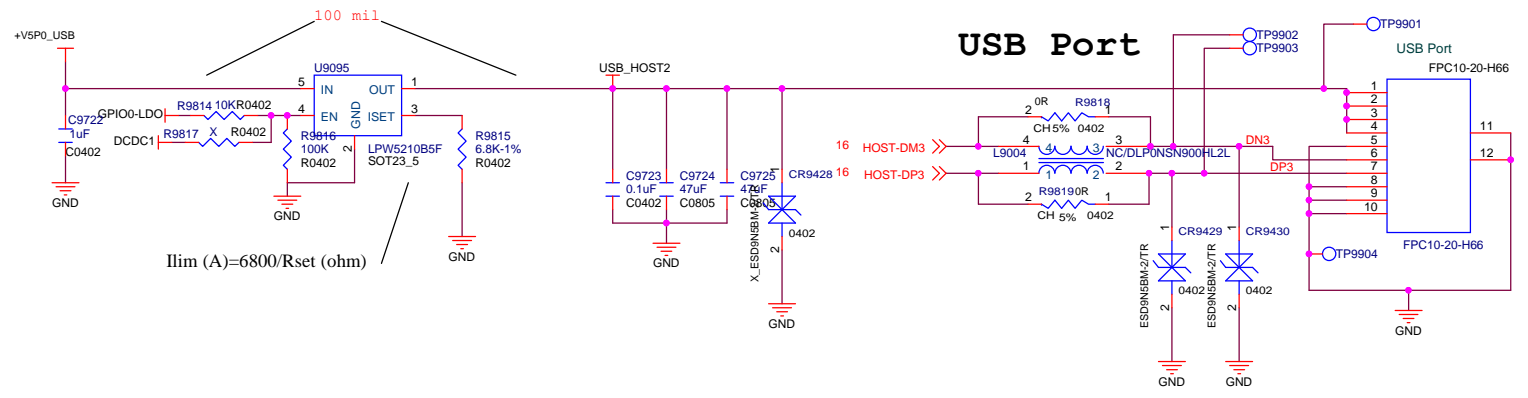
USB HOST



DOCKING



USB Port



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