

# Rock64 Port V3 Definition

ver3.0

## Pi-2 Connector

	3.3V	1	2	5V	
	GPIO2_D1 (I2C0_SDA)	3	4	5V	
	GPIO2_D0 (I2C0_SCL)	5	6	GND	
	GPIO1_D4 (CLK32KOUT_M1)	7	8	GPIO2_A0 (UART2_TX_M1)	
	GND	9	10	GPIO2_A1 (UART2_RX_M1)	
[IR_RX]	GPIO2_A2	11	12	GPIO2_C2 (I2S1_SCLK)	
	GPIO0_A0	13	14	GND	
[UART1_TX]	GPIO3_A4	15	16	GPIO3_A5	[UART1_RTS]
	3.3V	17	18	GPIO3_A6	[UART1_RX]
	GPIO3_A1 (SPI_TXD_M2)	19	20	GND	
	GPIO3_A2 (SPI_RXD_M2)	21	22	GPIO3_A7	[UART1_CTS]
	GPIO3_A0 (SPI_CLK_M2)	23	24	GPIO3_B0 (SPI_CSN0_M2)	
	GND	25	26	GPIO2_B4 (SPI_CSN1_M0)	
[PWM0]	GPIO2_A4 (I2C1_SDA)	27	28	GPIO2_A5 (I2C1_SCL)	[PWM1]
	GPIO2_A3	29	30	GND	
	GPIO0_A2	31	32	GPIO2_C6 (I2S1_SDIO3)	
	GPIO2_C5 (I2S1_SDIO2)	33	34	GND	
	GPIO2_C1 (I2S1_LRCKTX)	35	36	GPIO2_C4 (I2S1_SDIO1)	
	GPIO2_C0 (I2S1_LRCKRX)	37	38	GPIO2_C3 (I2S1_SDI)	
	GND	39	40	GPIO2_C7 (I2S1_SDO)	

Note: [xxx] is secondary function, [xxx] is new assigned GPIO definition compare with V2.

# Pi-5+ Connector

ver3.0

3.3V	1	2	5V
GPIO2_C1 (I2S1_LRCKTX)	3	4	GPIO2_C2 (I2S1_SCLK)
GPIO2_C7 (I2S1_SDO)	5	6	GPIO2_C3 (I2S1_SDI)
GND	7	8	GND
GPIO2_C0 (I2S1_LRCKRX)	9	10	GPIO2_B7 (I2S1_MCLK)
GPIO2_C5 (I2S1_SDIO2)	11	12	GPIO2_C4 (I2S1_SDIO1)
GPIO0_D3 (SPDIF_TX_M0)	13	14	GPIO2_C6 (I2S1_SDIO3)
GND	15	16	GND
Ethernet RD+	17	18	Ethernet RD-
Ethernet TX+	19	20	Ethernet TX-
<b>RESET</b>	21	22	<b>PWRON</b>

Note: [xxx] is secondary function, [xxx] is new assigned GPIO definition compare with V2.