<table>
<thead>
<tr>
<th>Revision</th>
<th>Description</th>
<th>Date</th>
<th>Drawn</th>
<th>Checked</th>
<th>Approved</th>
</tr>
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<tbody>
<tr>
<td>Ver 0.9</td>
<td>Release version</td>
<td>2016-08-08</td>
<td></td>
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</tr>
</tbody>
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**Title**

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- P04: DDR3 16X2
- P05: CPU
- P06: PMIC
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**VERSION HISTORY**
PINE64
SOPINE A64

BLOCK DIAGRAM

- DDR3
- DRAMC
- STORAGE
- SD/MMC0
- SD/MMC CARD
- OSC 32K
- OSC 24M
- IR-RX
- RSB
- IRQ
- AUDIO
- PCM0
- PCM1
- UART2
- USB1
- USB2.0 host
- USB2.0 Port
- USB2.0 Port
- USB1 host
- USB2.0 Port
- HDMI
- SDIO WIFI
- BT
- PCM1
- UART1
- SDC1
- SDC0
- EMAC/GEMAC
- RJ-45
- RMII/RGMII PHY
- AC Line
- AXP803
- Battery with temperature detect
- Voltage Supply 3.55V-4.35V
- Capacitive Touch Screen Controller
- Rear camera

SOPINE A64
Away from the board outline and sensitive signal. Avoid GND line. RTX0P, HTX1P, HTX2P, HTX0P were Kelvin connection.

CPVEE and VEE were Kelvin connection.

CPVEE, VEE, CPVB, BP, CP H, CP B were Kelvin connection.

Max 200mA.

CPVEE and VEE were Kelvin connection.

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