

VERSION HISTORY

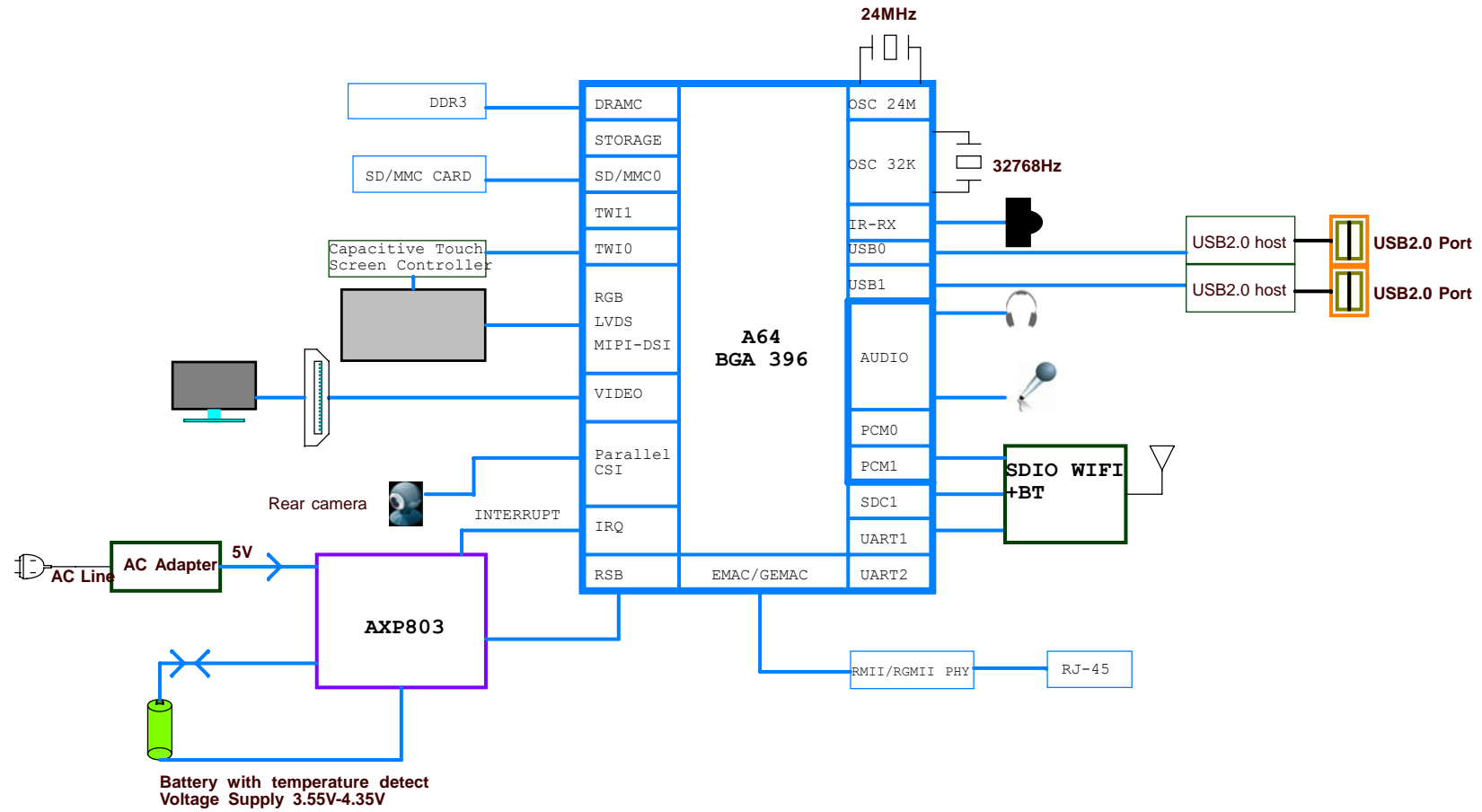
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Revision	Description	Date	Drawn	Checked	Approved
Ver 0.9	Release version	2016-08-08			

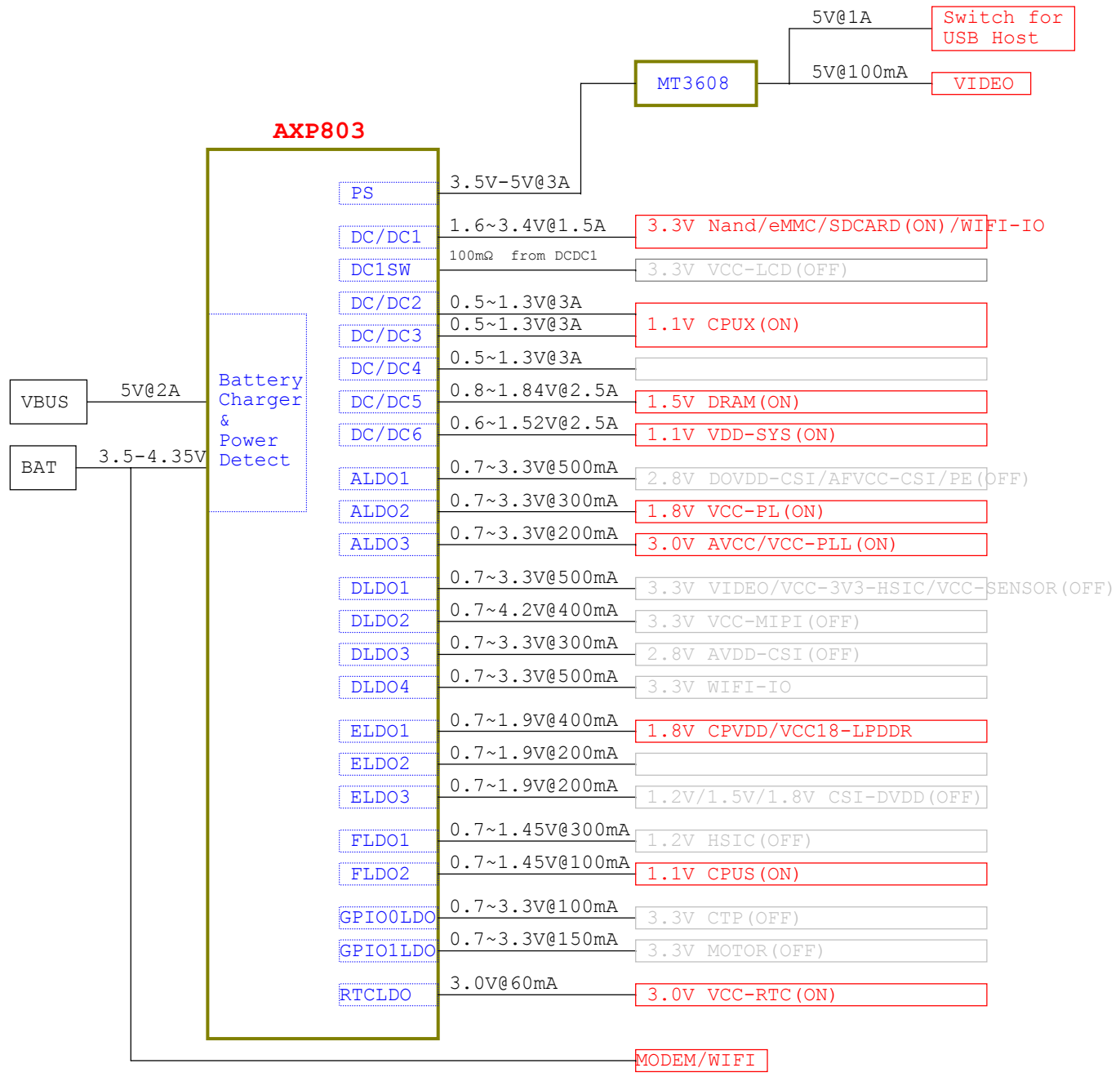
PINE64		
Title SOPINE A64		
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BLOCK DIAGRAM



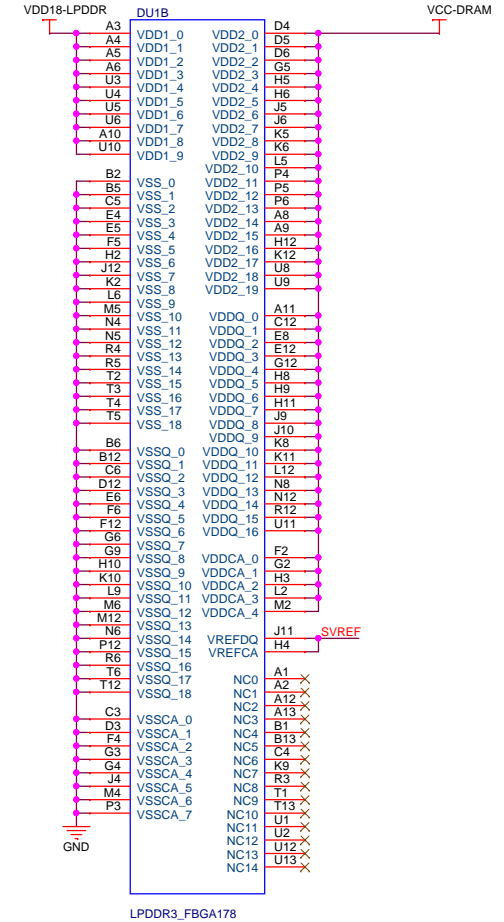
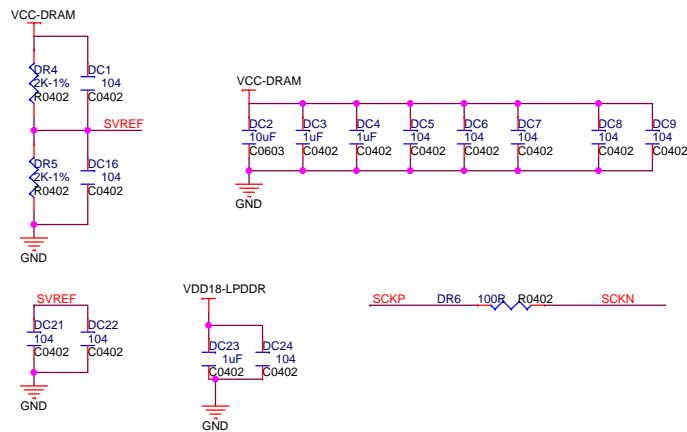
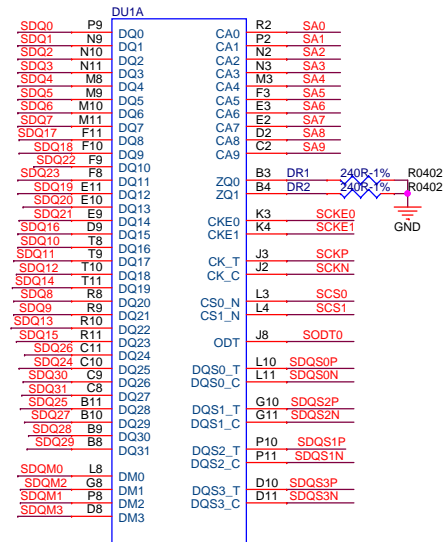
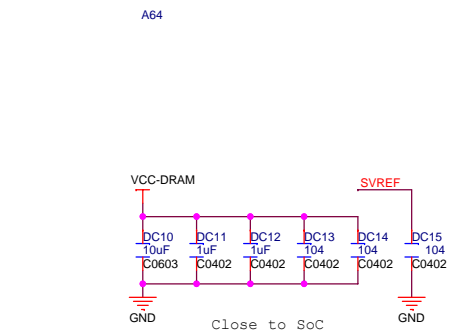
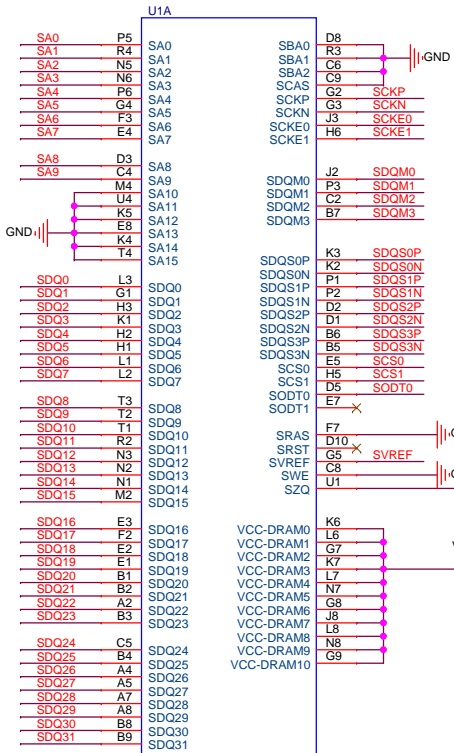
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Size: A3	Document Number: BLOCK DIAGRAM	Rev: 0.9
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POWER TREE



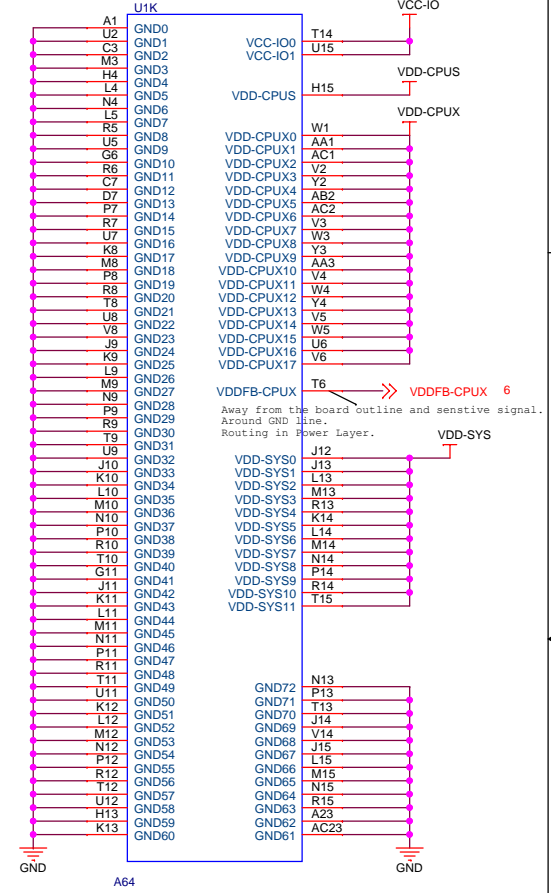
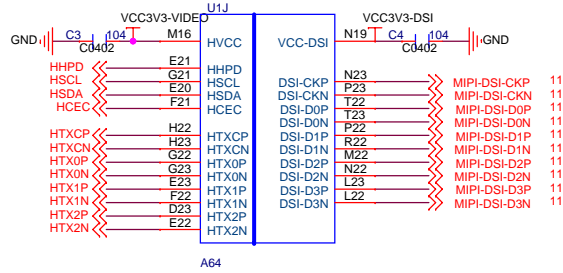
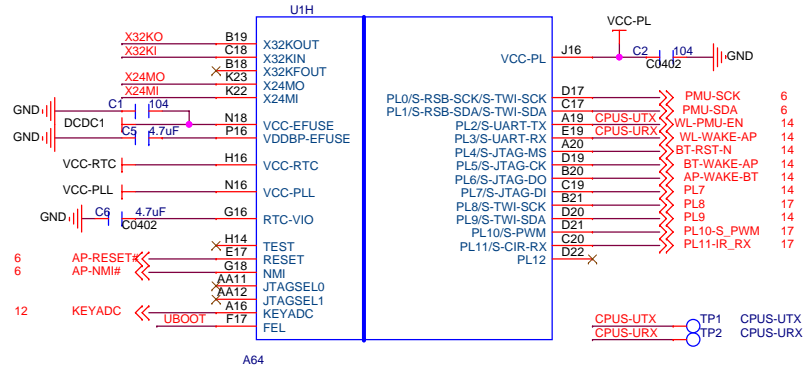
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LPDDR3 32X1

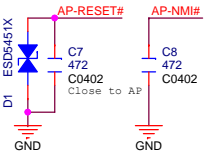


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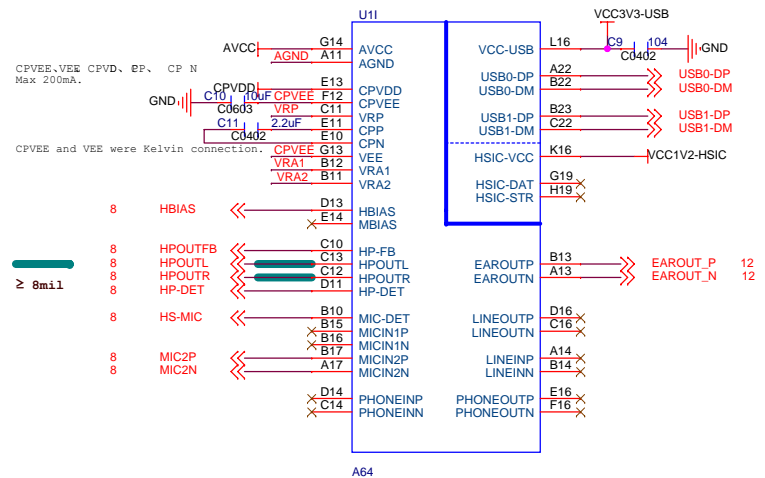
CPU



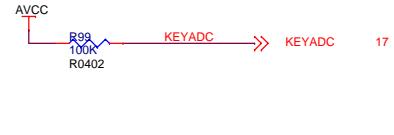
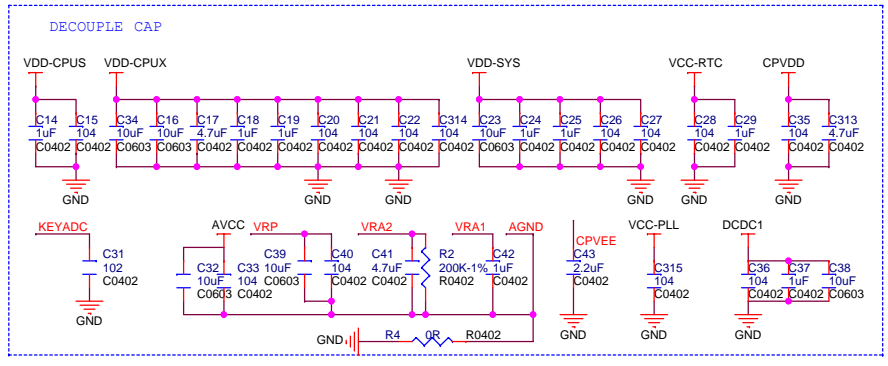
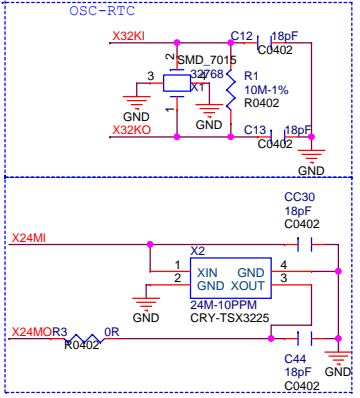
Away from the board outline and sensitive signal. Around GND line.



UBOOT TP1 TP2 TEST2

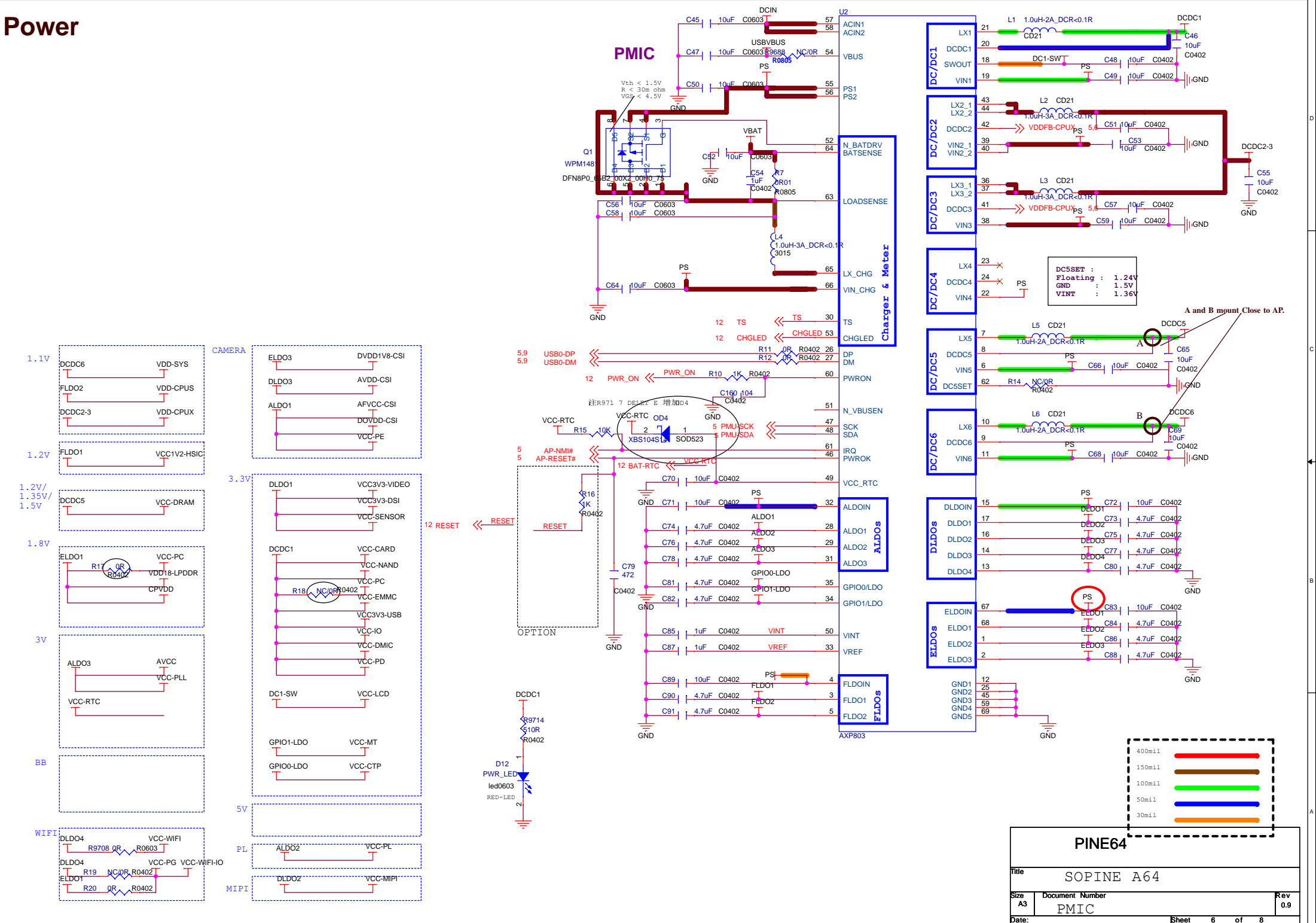


Away from the board outline and sensitive signal. Around GND line. Routing in Power Layer.



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Power



PMIC

Charger & Meter

DC/DC1

DC/DC2

DC/DC3

DC/DC4

DC/DC5

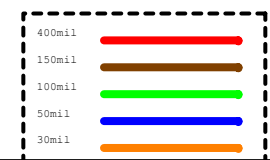
DC/DC6

ALDOs

ELDOs

FLDOs

DC5SET
 Floating : 1.24V
 GND : 1.5V
 VINT : 1.36V



PINE64

Title: **SOPINE A64**

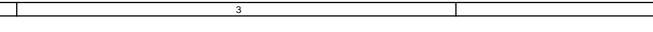
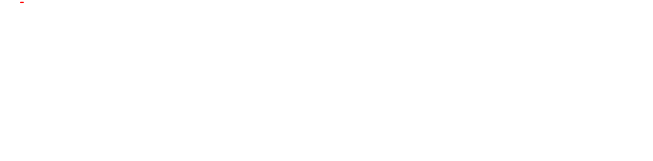
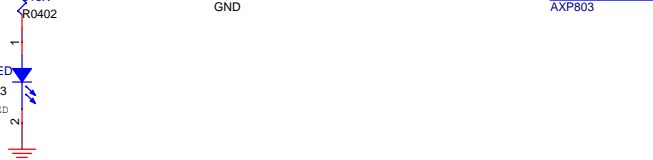
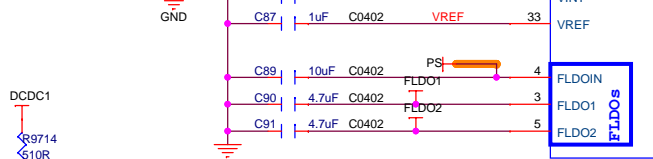
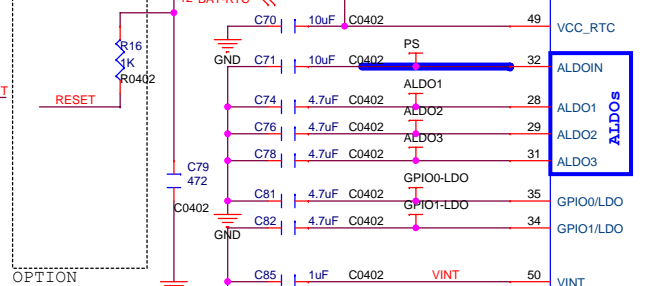
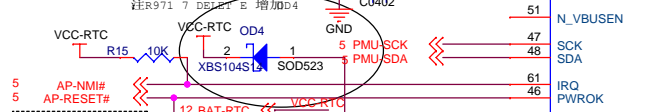
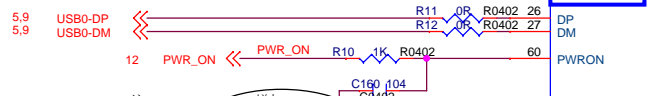
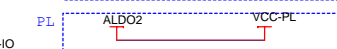
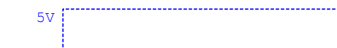
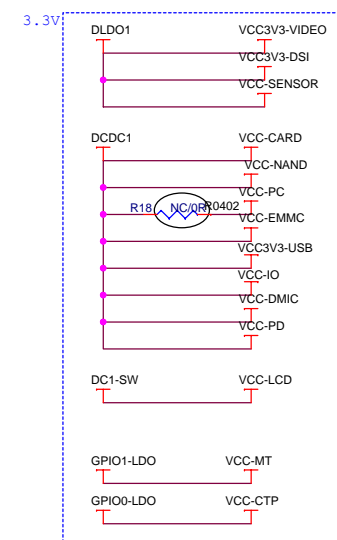
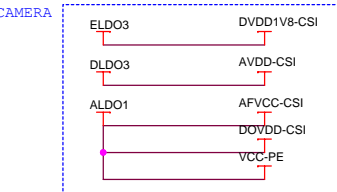
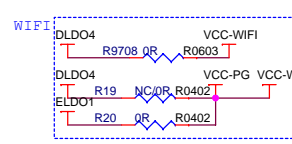
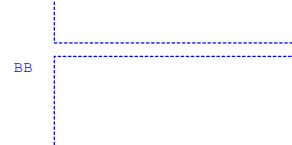
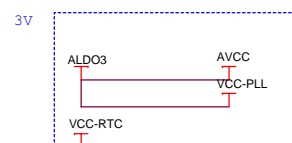
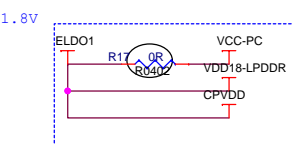
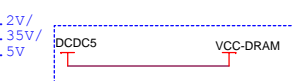
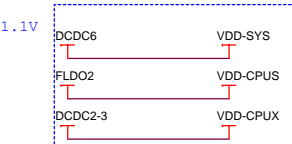
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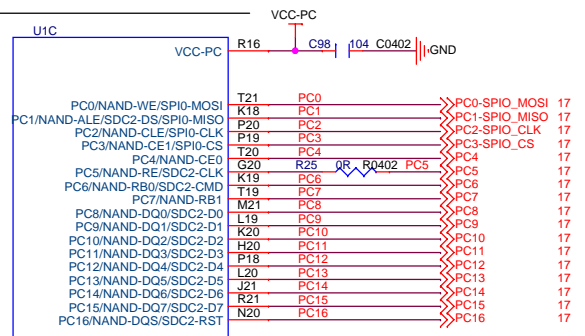
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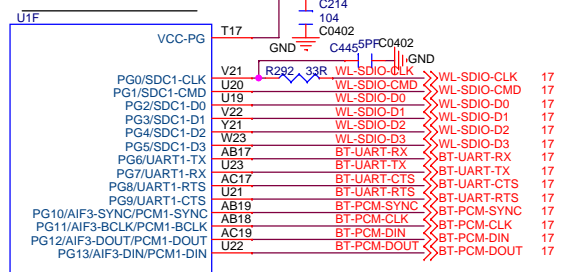
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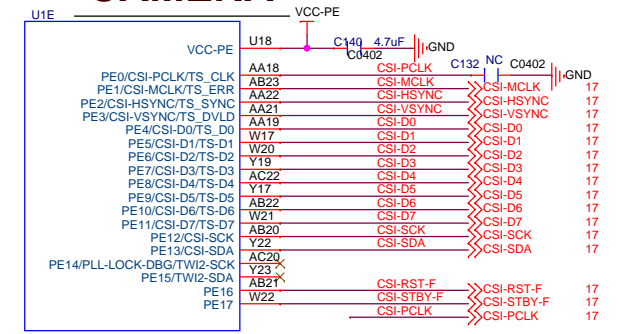
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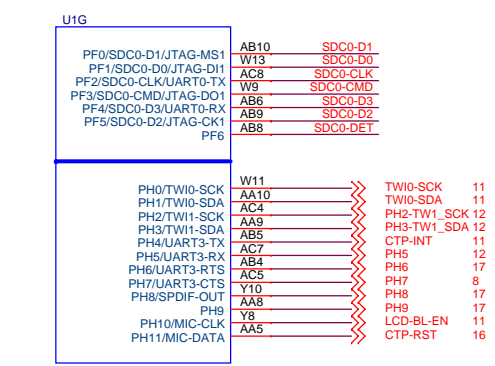
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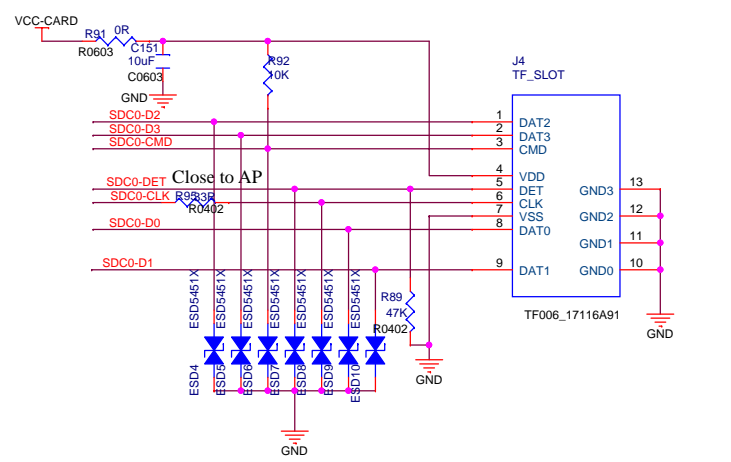
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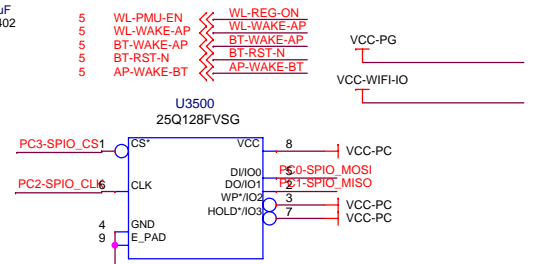
T-CARD/USB



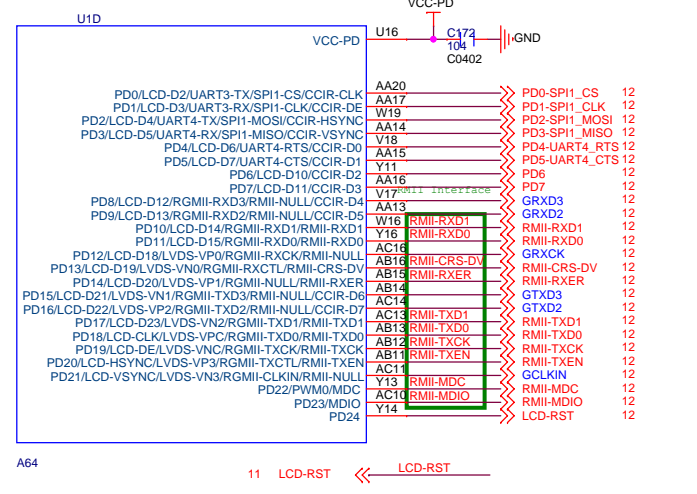
T-CARD



SPI FLASH



LCD



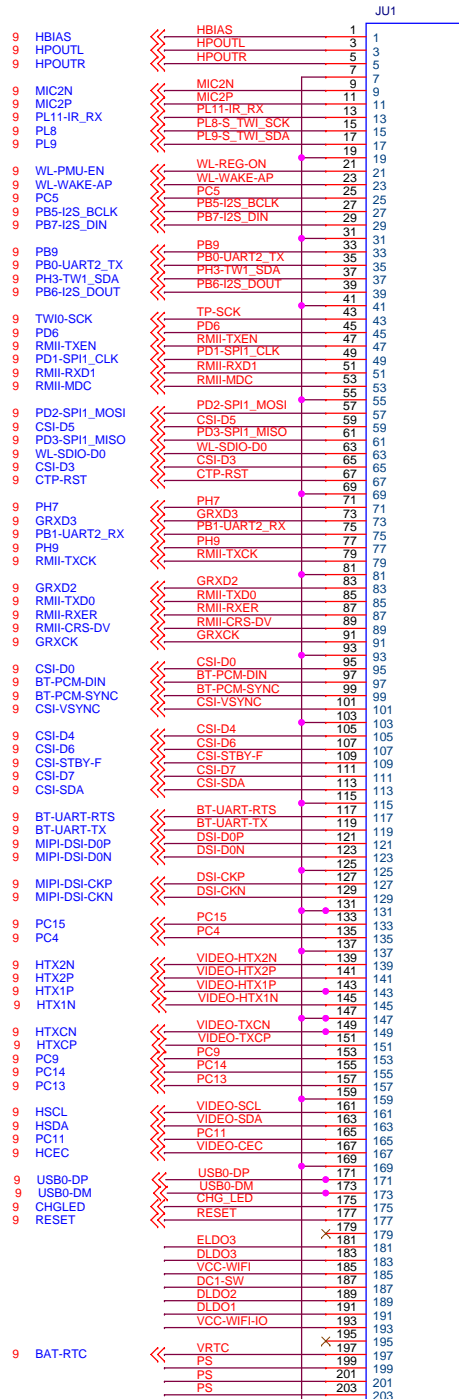
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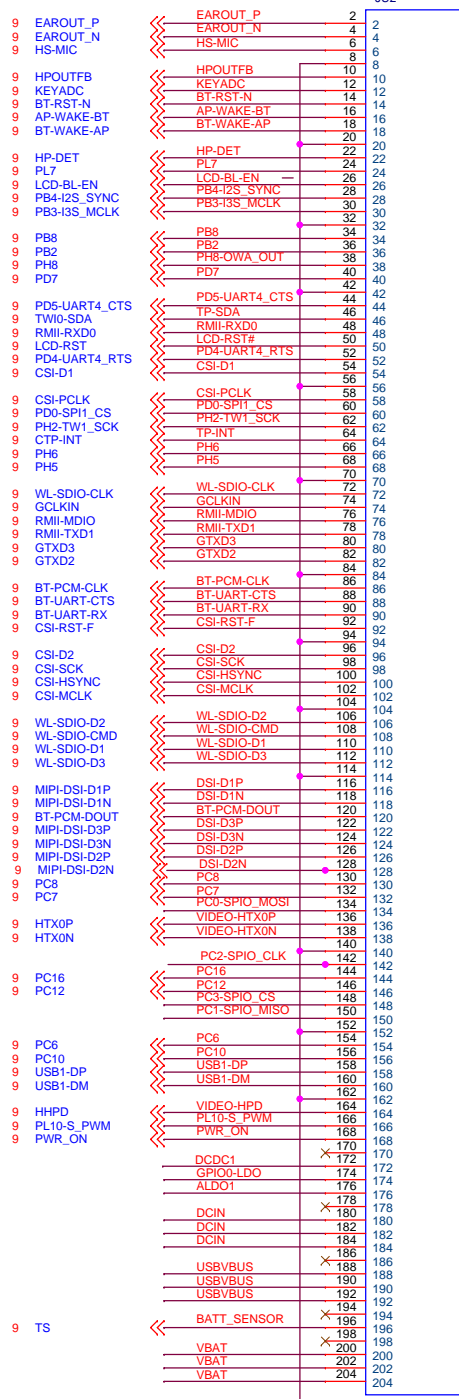
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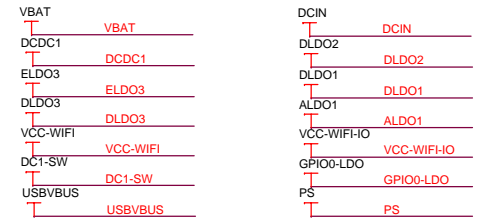
GOLD FINGER



PIC-203



PIC-204



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