Index

01.Index
02.Revision History
04.I2C MAP
05.Power diagram and Sequence
10.RK1808 Power
11.RK1808 OSC/PMUIO1/PMUIO2
12.RK1808 DDR Controller
13.RK1808 EMMC/SPI Controller
14.RK1808 USB/PCIe CONTROLLER
15.RK1808 SADC/KEY
16.RK1808 SDMMC0/SDMMC1
17.RK1808 MIPI DSI/CSI
18.RK1808 I2S0/I2S1
19.RK1808 GMII/LCDC/CIF
20.POWER-DC IN
21.POWER_PMIC RK809-2
38.RAM ddr4 4x16bit
41.EMMC
99.GOLD FINGER CONNECTION

Note

NOTE 1:
Component parameter description
1. DNP stands for component not mounted temporarily
2. If Value or option is DNP, which means the area is reserved without being mounted
3. If Flash is compatible, please notice when eMMC is used, the option is that @eMMC is mounted, @Nand is not mounted when Nand is used, the option is that @Nand is mounted, @eMMC is not mounted

NOTE 2:
Please use our recommended components to avoid too many changes. For more informations about the second source, please refer to our AVL.

Bill of Materials

Header:
Item\tPart\tDescription\tPCB Footprint\tReference\tQuantity\tOption

Combined property string:
{Item}\t{Value}\t{Description}\t{PCB Footprint}\t{Reference}\t{Quantity}\t{Option}
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<th>Version</th>
<th>Date</th>
<th>Author</th>
<th>Change List</th>
<th>Approved</th>
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<td>20181025</td>
<td></td>
<td>First edition for RK1800 ddr4</td>
<td>REF</td>
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<td>V2.0</td>
<td>20190919</td>
<td></td>
<td>SOEdge Schematic Released</td>
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# I2C MAP

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<tr>
<th>Port</th>
<th>Pin name</th>
<th>Domain</th>
<th>Bus name</th>
<th>Pull-up voltage</th>
<th>Slave Device</th>
<th>Slave Addr (MS 7Bits)</th>
<th>Note</th>
<th>Slave Bus Capability</th>
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**Project:** SOEdge Schematic 20190919 ver 2.0

**Date:** Thursday, September 19, 2019

**Designed by:**

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**Sheet:** 4 of 50
OSC_BPASS=0: 接24M的晶振。
OSC_BPASS=1: 外面接24M的CLK进来。

PMUIO1/OSC

PMUIO2

RK1808
EMMC/SFC Controller

SPI0 Controller
CIF/RGMII/LCDC Controller

10/100M原理和接法与1000M类似，唯一不同的RGMII_CLK=50M；需要注意的是10/100M的PHY_CRS_DV是接MAC_RXDV，而不是MAC_CRS管脚。

CIF Controller

LCDC Controller
For Daisy Chain Scheduling

2X16bit DDR4

Note: Place these decoupling capacitances close to the memory power supply balls

Remind: Refer to the latest AVL for parts selection.
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